

Comment 536 only partially implemented. Reject comment 536 and implement the resolution column.

Register	tu_3bp_01_0515.pdf proposal	Comments	New proposal	Resolution
1.7.5:0	Redefine "111101 = BASE-T1 PMA/PMD"	536, 496	Keep 111101 = 100BASE-T1 PMA/PMD Define 111100 = 1000BASE-T1 PMA/PMD	Approve in principle comment #496 Register 1.7.5 0 define 111100 = 1000BASE-T1 PMA/PMD
1.11.11	Adopt Table 45-14 from 802 3bw	536	tu_3bp_01_0515.pdf - OK	Let 3bw take care of it (already done) No action needed for .3bp
1.18.1	1=PMA/PMD is able to perform 1000BASE-T1 0=PMA/PMD is not able to perform 1000BASE-T1	536	tu_3bp_01_0515.pdf - OK	.3bw creates 1.18 (already done) Action: .3bp adds bit 1 See tu_3bp_01_0515.pdf page 7
1 2304.15	Remove PMA/PMD reset bit	536, 492	Propose keep both 1.0.15 and 1.2304.15 Add statement in 45.2.1.130a.1 as follows: Bit 1.2304.15 is a copy of 1.0.15. Setting either bit shall reset the 1000BASE-T1 PMA/PMD	Keep both 1.0.15 and 1.2304.15 Add statement in 45 2.1.130a.1 as follows: Bit 1.2304.15 is a copy of 1.0.15. Setting either bit shall reset the 1000BASE-T1 PMA/PMD, Implement comment 492
1 2304.11	Remove Low-power mode bit	536	Propose keep both 1.0.11 and 1.2304.11 Add statement in 45.2.1.130a.2 as follows: Bit 1.2304.11 is a copy of 1.0.11. Setting either bit shall put the 1000BASE-T1 PMA/PMD in low power mode.	Keep both 1.0.11 and 1.2304.11 Add statement in 45 2.1.130a.2 as follows: Bit 1.2304.11 is a copy of 1.0.11. Setting either bit shall put the 1000BASE-T1 PMA/PMD in low power mode.
1 2304.15	Changed to "Force mode enable"	536	Leave 1 2304.15 as reset Force mode bit not needed since force mode is implied when Autonegotiation is disabled or is not implemented. As an aside for 802.3bw 1 2100.15 should be removed.	Action: .3bp For registers 1 2100.14 and 1 2100.3:0 add the following sentence to the register description. The setting of this register shall be ignored when Auto-Negotiation is enabled.
1 2304.14	Changed to Master/Slave config	536, 496	Don't use 1.2304.14. Use 1.2100.14 as proposed in comment 496	Action: .3bp needs to change 100BASE-T1 to BASE-T1 for register 1.2100 Accept in principle comment 496
1 2304.4	Moved to same register bit 14	536, 496	Remove this bit since using 1.2100.14	Action: .3bp remove all references to register 1 2304.4
1 2304.3:0	Based on to 1 2100.3:0, add 1000BASE-T1	536, 496	Remove this bit. Use 1.2100.3:0 instead. 0001 = 1000BASE-T1	Action: .3bp needs to change 100BASE-T1 to BASE-T1 for register 1.2100 .3bp define 0001 setting to 1000BASE-T1 .3bp remove all references to 1.2304.3:0
3 2304.15	Remove PCS reset bit	536, 492, 493, 449, 450, 451, 452	Propose keep both 3.0.15 and 3.2304.15 hence comments 492, 493, 449, 450, 451, 452 must be implemented Add statement in 45.2 2.50a.1 as follows: Bit 3.2304.15 is a copy of 3.0.15. Setting either bit shall reset the 1000BASE-T1 PCS.	Keep both 3.0.15 and 3.2304.15 hence comments 492, 493, 449, 450, 451, 452 must be implemented Add statement in 45 2.2.50a.1 as follows: Bit 3.2304.15 is a copy of 3.0.15. Setting either bit shall reset the 1000BASE-T1 PCS. Implement comments 492, 493, 449, 450, 451, 452
3 2304.14	Remove loopback bit	536, 494, 449, 450, 451, 351, 357	Propose keep both 3.0.14 and 3.2304.14 hence comments 492, 493, 449, 450, 451, 452 must be implemented Add statement in 45.2 2.50a.2 as follows: Bit 3.2304.14 is a copy of 3.0.14. Setting either bit shall enable loopback.	Keep both 3.0.14 and 3.2304.14 hence comments 492, 493, 449, 450, 451, 452 must be implemented Add statement in 45 2.2.50a.2 as follows: Bit 3.2304.14 is a copy of 3.0.14. Setting either bit shall enable loopback. Implement comments 494, 449, 450, 451. Comments 351, 357 approved in principle per 494.
7 512.12			Typo	Table 45-211a 7 512.12 should not be self clear. Revmoe SC