Suggested Remedy for Comment 555
> **define a bit for Transmit Disable in 1.2304.14 instead of using 1.9.0 Global Transmit Disable**

Page 29 line 36 add new row
"1.2304.14   Transmit Disable   1 = Transmit Disable 0 = Normal operation   R/W"
on page 30 line 10 add new paragraph
"45.2.1.130a.2 BASE-T1 PMA transmit disable (1.2304.14)
When bit 1.2304.14 is set to a one, the PMA shall disable output on the transmit path.
When bit 1.2304.14 is set to a zero, the PMA shall enable output on the transmit path."
Then change "1.2304.14:4" to "1.2304.13:4"

Suggested Remedy for Comment 563
> **reconcile table 97-5 and 97-6 with the latest register definitions**
> **do not use fault registers in the base addresses**

Table 97–5—MDIO/PMA control variable mapping

MDIO control variable	PMA register name	Register/bit number	PMA control variable
Reset	Control register 1 / BASE-T1 PMA control register	1.0.15 / 1.2304.15	PMA_reset
~~Global~~ Transmit disable	~~Transmit disable register~~ BASE-T1 PMA control register	~~1.9.0~~ 1.2304.14	~~Global~~ PMA_transmit_disable

Table 97–6—MDIO/PMA status variable mapping

MDIO control variable	PMA register name	Register/bit number	PMA status variable
~~Fault~~	~~Status register 1~~	~~1.1.7~~	~~PMA_fault~~
~~Transmit fault~~	~~Status register 2~~	~~1.8.11~~	~~PMA_transmit_fault~~
Receive fault	1000BASE-T1 Status register ~~2~~	~~1.8.10~~ 1.2305.1	PMA_receive_fault