

IEEE P802.3bq D3.1 25G/40GBASE-T Ethernet 2nd Sponsor recirculation ballot comments

Cl 113 SC 113.3.2.3.3 P 117 L 52 # r02-1
 RAN, ADEE Intel Corporation

Comment Type T Comment Status A Invalid Blocks

"Invalid blocks are replaced by error"

"Error" is defined in 113.3.2.2.14 (in the transmit function) as a character. An invalid block should be replaced by 8 error characters.

Also, it is not obvious that this definition applies to 113.3.2.3.3 (receive function) - it appears in a different area, 12 pages before this subclause..

Figure 113-20 already specifies that error blocks are decoded as EBLOCK_R, so it may be easiest to use that; invalid block should be assigned R_BLOCK_TYPE value E.

SuggestedRemedy

Change
 "Invalid blocks are replaced by error"

to
 "R_BLOCK_TYPE of invalid blocks is set to E"

or
 "Invalid blocks are decoded as EBLOCK_R as shown by the PCS Receive state diagram (Figure 113-20)."

Response Response Status C
 ACCEPT IN PRINCIPLE.

Delete:
 "Invalid blocks are replaced by error."

Insert at end of subclause (P117 L11):
 "R_BLOCK_TYPE of an invalid block is set to E."

Cl 113 SC 113.3.2.3.3 P 118 L 7 # r02-2
 RAN, ADEE Intel Corporation

Comment Type T Comment Status A Invalid Blocks

"The block contains the payload of an invalid received PHY frame"

The payload spans multiple blocks, so a block can't contain the payload.

SuggestedRemedy

Change item e from
 "The block contains the payload of an invalid received PHY frame or the first 64B/65B block following an invalid received PHY frame"

to
 "The block is part of the payload of an invalid received PHY frame, or is the first 64B/65B block following an invalid received PHY frame".

Response Response Status C
 ACCEPT IN PRINCIPLE.

Change "The block contains the payload of an invalid received PHY frame..."

to "The block contains information from the payload of an invalid received PHY frame..."

IEEE P802.3bq D3.1 25G/40GBASE-T Ethernet 2nd Sponsor recirculation ballot comments

Cl 113 SC 113.3.2.3.3 P 117 L 52 # r02-3
 RAN, ADEE Intel Corporation

Comment Type E Comment Status R Invalid Blocks

A compliant transmitter will never send blocks matching any of items a-d, since the T_BLOCK_TYPE calculation would return E which would result in EBLOCK_T being sent.

With FEC protecting all bits, it should be extremely rare that data corruption causes blocks to match any of these criteria without also causing parity check failure.

Therefore, the prevalent (if not sole) reason for marking blocks as invalid in the receiver is going to be FEC parity check failure (covered by item e and the last paragraph).

Restructuring this subclause may improve clarity.

SuggestedRemedy

Move the last paragraph (starting with "The PCS Receive function") to be the first paragraph in this subclause.

Re-order the list so that item e (starting with "The block contains the payload of an invalid received PHY frame") appears first.

Move the sentence "Invalid blocks are replaced by error" (or its replacement, subject to another comment) to the end of this subclause.

Response Response Status C

REJECT.

The text is correct, as is.

While it may (or may not) be extremely rare, data corruption can occur, especially because decoding algorithms for LDPC are generally suboptimal, and subject to significant variation between implementations.

Cl 113 SC 113.3.2.3.3 P 12 L 2 # r02-4
 RAN, ADEE Intel Corporation

Comment Type T Comment Status A Invalid Blocks

List item d can't be encountered by a receiver - the *GMII characters are decoded from the block format, not the other way around.

Items b and c seem be written as if they belong in a transmitter. The receiver test for control characters is much more detailed, as shown in the definition of R_BLOCK_TYPE. R_BLOCK_TYPE also covers item a.

The criteria in items a-d can be replaced by the simple condition "R_BLOCK_TYPE of the block is E".

SuggestedRemedy

Delete list item a-d.

Unless the handling of invalid blocks is changed here to "set R_BLOCK_TYPE to E" (subject of another comment):

add an item:

a) R_BLOCK_TYPE of the block is E.

Response Response Status C

ACCEPT IN PRINCIPLE.

Delete list item d:

d)The set of eight 25GMII/XLGMII characters does not have a corresponding block format in Figure 113-9, for 25GBASE-T, or Figure 113-10 for 40GBASE-T.

Rationale:

List items a, b, and c are unrelated to the move and can occur as the result of a (presumably rare) data error which might be undetected by other means.

List item d is related to the move itself, as the conversion from 25GMII/XLGMII characters to block formats only happens at the transmitter, and this text is now in the PCS receive subclause.

R_BLOCK_TYPE being E is the result of an invalid block, not an invalid block type in itself.

IEEE P802.3bq D3.1 25G/40GBASE-T Ethernet 2nd Sponsor recirculation ballot comments

Cl 113 SC 113.5.4.2 P 13 L 26 # r02-5
 RAN, ADEE Intel Corporation

Comment Type T Comment Status A EZ

Also applies to 113.5.3.5.

The text in these subclauses hasn't changed, but should have changed based on comment r01-9.

Symbol rate units are Baud, not Hz.

SuggestedRemedy

Change "MHz" to "MBd" in both places.

Response Response Status C

ACCEPT.

Cl 113 SC 113.7.1 P 175 L 36 # r02-6
 Hajduczenia, Marek Bright House Network

Comment Type E Comment Status A

Extra >>"<< in "ISO/IEC TR 11801-9905 ""Guidelines" text

SuggestedRemedy

Change "ISO/IEC TR 11801-9905 ""Guidelines" to "ISO/IEC TR 11801-9905 "Guidelines"

Response Response Status C

ACCEPT.
 Duplicate by comment r02-7

Cl 113 SC 113.7.1 P 175 L 36 # r02-7
 Maguire, Valerie The Siemon Company

Comment Type E Comment Status A EZ

Extra quote marks.

SuggestedRemedy

Replace, ""Guidelines with "Guidelines

Response Response Status C

ACCEPT.
 Duplicate of comment r02-6

Cl 45 SC 45.2.3.6.1 P 42 L 32 # r02-8
 Anslow, Peter Ciena Corporation

Comment Type E Comment Status A EZ

The text "The PCS type abilities of the PCS are advertised in bits 3.8.9 and 3.8.6:0." is not correct as 802.3by uses bit 3.8.7.

SuggestedRemedy

Change to "The PCS type abilities of the PCS are advertised in bits 3.8.9 and 3.8.7:0."

Response Response Status C

ACCEPT.

Cl 113 SC 113.3.6.2.4 P 125 L 52 # r02-9
 Mcclellan, Brett Marvell Semiconducto

Comment Type T Comment Status A LATE

"b) a block type field 0x4B, a valid O code, and zeros, as shown in Figure 82-4;"
 Figure 82-4 is the wrong reference. Rather than point to Figure 113-10 (the correct figure), I think this reference is unnecessary and can be deleted.
 The same reference error appears on page 126 line 39.

SuggestedRemedy

Page 125 line 52 delete ", as shown in Figure 82-4"
 page 126 line 39 delete "as shown in Figure 82-4"

Response Response Status C

ACCEPT.

Cl 113 SC 113.3.2.2.6 P 103 L 28 # r02-10
 Mcclellan, Brett Marvell Semiconducto

Comment Type E Comment Status A LATE

Table 113-1 starts on page 102 and continues on page 103 at line 28, however Figure 113-10 appears between the split. This was introduced in draft 3.1.

SuggestedRemedy

Beat Framemaker into submission and move Figure 113-10 out of the split table.

Response Response Status C

ACCEPT.