

# 802.3bq PHY Baseline Ad Hoc Report

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# Status

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- One meeting held 19 February 2015
- Focus on closing remaining technical items in draft 1.2
  - Common Mode Rejection text in separate ad hoc
- One presentation:
  - Zimmerman\_3bqah\_01\_0215.pdf
  - Updated to include consensus outcomes (01a)
    - Selected slides follow, indicating consensus in CAPS
- No future meetings planned.

# Ad hoc progress:

## Summary of known issues (1)

- Table 80-3 Maximum pause quanta needs number
  - Propose 50 pause quanta = 25600 BT/512 BT/quanta (OK)
- Whether FR should be made a mandatory capability (which can be disabled by register), remain optional, or be deleted (various places in the text)
  - A partial hole (only in 113.1), flagged by Ed Note, propose (CONCLUSION: FR TO REMAIN OPTIONAL)
- PBO level at which startup begins (see Ed note at 113.4.2.5.14)
  - Not a hole, flagged by Editor's note in 113.4.2.5.14, propose keep at 8 dB
  - (DEAL WITH IN FIRST WG BALLOT CYCLE – DELETE NOTE)

# Summary of known issues (2)

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- Whether we should continue to support 8 levels of PBO up to 14 dB of PBO (maximum required to select is 6dB)
  - Not a hole, not flagged, propose keep at 14 dB
  - (DEAL WITH IN FIRST WG BALLOT CYCLE IF NECC.)
- Whether the transmitter linearity specification is needs reformulating
  - Not a hole, flagged by Ed note in 113.5.3.2, equation 113-9, no proposal but what is there is probably wrong (KEEP EDITOR'S NOTE)
- The lowest frequency we care about cabling delay
  - Not a hole, flagged by Ed note at 113.5.4.6.10 & 113.7.2.5, propose keep as is – (REMOVE EDITOR'S NOTE)

# Summary of known issues (3)

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- Fast wake support required for 40G PHYs that support EEE
  - Not a hole, but an error, not flagged, discuss options
    - A) Add edit to clause 78 exempting BASE-T from the requirement
    - B) Add register and autoneg bits for EEE fast wake, and equate all EEE references to EEE deep sleep (which they are) (one line of text).
  - (IMPLEMENT OPTION A, AND ASK SYSTEM VENDORS TO LOOK INTO WHETHER THIS IS OK)