

# Logic Ad Hoc Report

**IEEE P802.3bs 400 Gb/s Ethernet Task Force**

May 2014 Norfolk

Mark Gustlin - Xilinx

# 400GbE Logic Ad Hoc Priorities

- The stated charter is: **Work on technical proposals related to the 400GbE Architecture**
- Dates of future logic ad hoc meetings will be announced via the reflector

# Meeting Details

- The 400 Gb/s Ethernet Task Force Logic Ad hoc:
  - Held 1 meeting on April 23<sup>rd</sup>
- Reviewed 3 presentations:
  - Update of Bit multiplexing in 400GbE PMA – Xinyuan Wang
  - Evaluation of FEC Performance with Symbol and Bit muxing Scenarios – Tongtong Wang
  - PCS/PMA Architecture and OTN Support Proposal – Steve Trowbridge
- Meeting minutes and presentations can be found at:  
<http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml>

**Thanks!**