

Logic Ad Hoc Report

IEEE P802.3bs 400 Gb/s Ethernet Task Force

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400GbE Logic Report

- The stated charter is: **Work on technical proposals related to the 400GbE Architecture**
- Dates of future logic ad hoc meetings will be announced via the reflector
- The 400 Gb/s Ethernet Task Force Logic Ad Hoc held one meeting on October 21st

400GbE Logic Ad Hoc Presentations (10/21)

- **FEC Architecture** – John D’Ambrosia/Gary Nicholl
- **FEC Core Area Comparison and Model** - Martin Langhammer
- **FEC Considerations for 400GE and Beyond** – Martin Langhammer
- **FEC Structure for 400GbE** – Zhongfeng Wang

Thanks!