

# Chief Editor's report

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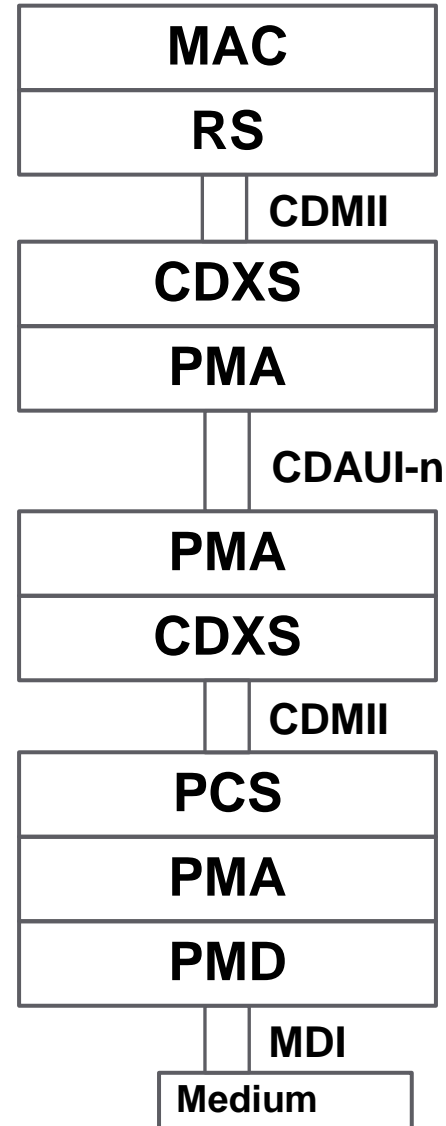
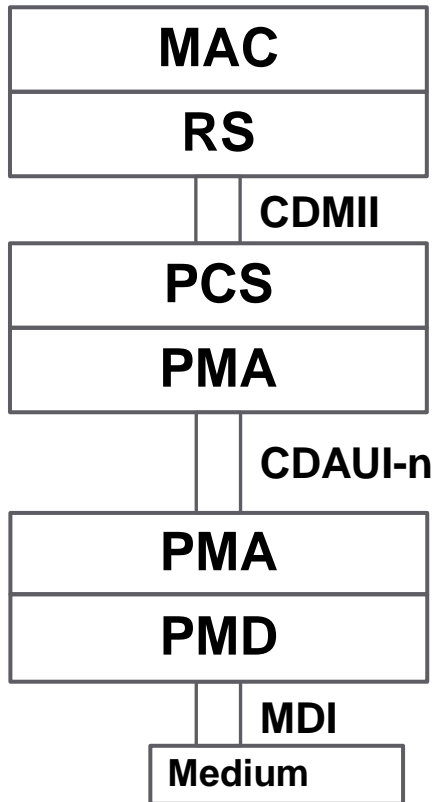
IEEE P802.3bs Task Force, Berlin, Germany, March 2015

# Introduction

The following slides contain:

- an initial proposal for a structure for the P802.3bs 400 Gb/s Ethernet draft amendment
- some comments on naming
- a list of open questions for CDAUI-16

# Adopted stack



When PCS functions and CDXS functions do not match

# New clauses

Clause	Content	Baseline
200*	Introduction to 400 Gb/s networks	
201	RS and MII for 400 Gb/s operation	
202	CDMII extender (includes CDXS)	Baseline adopted
203	PCS including FEC**	Need baseline
204	PMA	Need baseline
205	400GBASE-SR16	Baseline adopted
206	PMD clause for 500 m objective	Need baseline
207	PMD clause for 2 km objective	Need baseline
208	PMD clause for 10 km objective	Need baseline
204A	Partitioning examples (informative)	
204B	CDAUI-16 chip-to-chip (normative)	Baseline adopted
204C	CDAUI-16 chip-to-module (normative)	Baseline adopted
204D	CDAUI-8 chip-to-chip (normative)	Need baseline
204E	CDAUI-8 chip-to-module (normative)	Need baseline

\*The starting clause number has not been allocated yet. Clause 200 is a placeholder.

\*\*It has been suggested that this could be in Annex 203A in anticipation of other PCSs.

# Amended clauses

Clause	Change	
1	Add new references, definitions, abbreviations	
4	Add 400G parameters to Table 4-2	
30	Add new management objects / attributes	
45	Add new registers / bits	
78	Add new EEE PHYs	Baseline adopted
Annex A	Add any new bibliography entries	
Annex 4A	Add / modify note to Table 4A-2	
Annex 31B	Add 400G PAUSE information	

# Naming

- Motion #3, July 14: Move to adopt the baseline for the CDMII logical interface as shown in slide 5 of [gustlin\\_3bs\\_03\\_0714.pdf](#)
- Motion #3, Jan 15: Move to adopt slides 4 and 8 from [dambrosia\\_3bs\\_02b\\_0115](#) as baseline architecture
  - Slide 8 has CDMII, CDXS, CDAUI-n

The above motions have adopted the names:

CDMII for the 400 Gigabit Media Independent Interface

CDXS for the CDMII extender sublayer

CDAUI-n for the 400 Gigabit Attachment Unit Interface

# Alternative naming

In the P802.3by 25 Gb/s Ethernet Task Force there has been an agreement to adopt:

25G-MII for the 25 Gigabit Media Independent Interface

25G-AUI for the 25 Gigabit Attachment Unit Interface

In preference to the Roman numeral versions of XXVGMII and XXVAUI

Changing the adopted names from the previous page to this convention would result in: 400G-MII, 400G-XS, 400G-AUI-8 and 400G-AUI-4 which seem worse than the existing names (particularly for the AUI).

The author therefore proposes to retain the existing names and maintain consistency with 10G, 40G and 100G names rather than change them.

# Open questions for CDAUI-16

## CDAUI-16 chip-to-chip

- BER requirement is TBD
- The optional transmitter equalization feedback for CAUI-4 chip-to-chip can use MDIO registers 1.180 to 1.187 (8 registers). Should CDAUI-16 define 32 new registers for this purpose or 24 new and re-use the 8 existing CAUI-4 registers?

## CDAUI-16 chip-to-module

- BER requirement is TBD
- CDAUI-16 will need a 16 way connector instead of a 4 way connector. Are there any changes required to the spec. to account for this?
- Annex 83E for CAUI-4 chip-to-module requires that the host provides a “recommended CTLE peaking value”. There was some discussion before the baseline was adopted as to whether this would also be required for CDAUI-16.



Thanks!