

# Logic Ad Hoc Report

**IEEE P802.3bs 400 Gb/s Ethernet Task Force**

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# 400GbE Logic Report

- The stated charter is: **Work on technical proposals related to the 400GbE Architecture**
- Dates of future logic ad hoc meetings will be announced via the reflector
- The 400 Gb/s Ethernet Task Force Logic Ad Hoc held two meetings, one on June 19<sup>th</sup> and another on June 29<sup>th</sup>

# 400GbE Logic Ad Hoc Presentations (6/19)

- 400GE FEC Implementation - Martin Langhammer
- Considerations for breakout - Martin Langhammer
- 1x400G vs 4x100G FEC Implications – Bill Wilkie
- Analysis on 400G FEC Architecture – Phil Sun

# 400GbE Logic Ad Hoc Presentations (6/29)

- Update and further study on FEC Architecture proposal - Xinyuan Wang
- 400G FEC complexity discussion – Tongtong Wang
- Further Analyses of Reducing Complexity for Data Alignment – Zhongfeng Wang

**Thanks!**