

Logic Ad Hoc Report

IEEE P802.3bs 400 Gb/s Ethernet Task Force

November 2015 Dallas

Mark Gustlin – Xilinx

400GbE Logic Report

- The stated charter is: **To address all issues in relation to the overall architecture of IEEE P802.3bs to ensure progress towards a technically complete draft**
- Dates of future logic ad hoc meetings will be announced via the reflector
- The 400 Gb/s Ethernet Task Force Logic Ad Hoc held one meeting on October 23rd

400GbE Logic Ad Hoc Presentations (6/19)

- **Toward convergence of FEC interleaving schemes for 400GE – Zhongfeng Wang**
- **FEC codeword filling performance – Pete Anslow**

Thanks!