

P802.3bs Electrical Track Closing Report

January 19th 2016 Atlanta GA

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Progress

- Comment resolution
 - Resolved the outstanding 33 comments for Annexes 120D & 120E allocated to the electrical track
- Met for 325 minutes. 9.8 minutes per comment.
- Thanks to the Editor and Electrical track members for consensus building in preparation for and during this meeting

Items decided upon Annex 120D (C2C)

- Updated linear fit method exceptions (comments #83,29)
- Use PRBS13Q for signal level measurements (comment #80)
- Align R_LM value in Table 120D-1 with COM value (comments #78,117,28)
- Adopted $1e-4$ Symbol error rate (comment #122)
- A_v, A_fe, and A_ne values to be 0.45, 0.45, and 0.63 V (black) respectively (comments #82,31)
- Adopted 3dB COM margin (comment #111)

Items decided upon Annex 120E (C2M)

- Adopted $1e-5$ Bit error rate (comments #61, 113)
- Adopted Host transition time of 10ps & Module transition time of 9.5ps (comments #151,152)
- Adopted a module output test crosstalk generator transition time of 19ps (comment #150)
- Adopted $\frac{1}{2}$ dB Step reference CTLE with LFEQ (comments #23,60)

Areas for further work

- R_LM measurement method
- SNR_Tx/SNDR values
- Allow for use of a module transmitter TXFIR in eye measurement method
- Additional Jitter tolerance measurement frequencies
- Pattern generator Jitter values
- Host Even-Odd Jitter value