

Logic Ad Hoc Report

**IEEE P802.3bs 200 Gb/s and 400 Gb/s Ethernet Task
Force**

November 2016 San Antonio

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400GbE Logic Report

- The stated charter is: **To address all issues in relation to the overall architecture of IEEE P802.3bs to ensure progress towards a technically complete draft**
- Dates of future logic ad hoc meetings will be announced via the reflector
- The 400 Gb/s Ethernet Task Force Logic Ad Hoc held one meeting

400GbE Logic Ad Hoc Presentations (10/27/16)

- 200GbE alignment marker characteristics - Pete Anslow, Ciena
- SSPR generation - Pete Anslow, Ciena
- Autocorrelation of PRBS13Q – Yasuo Hidaka, Fujitsu

Thanks!