

Electrical Interface Ad-hoc Report

IEEE P802.3bs 200 & 400Gb/s Ethernet Task Force

July 2017, Berlin

Electrical Ad Hoc Report

- The charter of the Electrical Interface Ad hoc is:
 - Address all issues in relation to the electrical interfaces to ensure progress towards a final draft.
 - *Identify issues or omissions in the adopted Baselines*
 - *Find consensus now, rather than in comment resolution.*

- 3 joint ad hoc meetings (with P802.3cd) & 1 electrical ad hoc meeting held since the May interim
 - 8 technical presentations
 - All presentations & meeting minutes are available on the ad hoc webpage :
 - <http://www.ieee802.org/3/bs/public/adhoc/elect/index.shtml>

Joint Ad Hoc Presentations 14th June

Resolving COM and Rx ITT concerns	Adee Ran	ran_061417_3cd_adhoc-v2.pdf
Tightening Channel Variation by Nominal Impedance Values for COM Package Model	Yasuo Hidaka	hidaka_061417_3cd_01_adhoc.pdf
Return Loss of Test Channel for Rx Interference Tolerance Test (Rx ITT)	Yasuo Hidaka	hidaka_061417_3cd_02_adhoc-v2.pdf

Joint Ad Hoc Presentations 21st June

Open electrical issues	Piers Dawe	dawe_062117_3cd_adhoc.pdf
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Joint Ad Hoc Presentations 28th June

Package impedance and termination effect on COM (update)	Mike Dudek	dudek_062817_3cd_adhoc.pdf
Concerns with stressed input tests in Annex 120E D3p2	Steve Sekel	sekel_062817_3cd_adhoc.pdf

Electrical Interface Ad Hoc Presentations 6th July

Ad-hoc Opening/Agenda/Work Items	Andre Szczepanek	szczepanek_01_070617_elect
Making Jitter Specs Compatible	Piers Dawe	dawe_01_070617_elect
C2M Spec Consistency And Tolerancing	Piers Dawe	dawe_3bs_01_0717.pdf