Unapproved minutes P802.3bs 400 Gb/s Ethernet Electrical Ad Hoc Teleconference 7 December 2015

Minutes taken by Andre Szczepanek, Inphi

The meeting started at 8:05 am Pacific chaired by Andre Szczepanek, the attendee list was taken from the Webex attendee list, plus any e-mail notifications of attendance.

Documentation for the call can be found at the Ad Hoc web page: http://www.ieee802.org/3/bs/public/adhoc/elect/index.shtml

Andre reminded everyone of the updated IEEE patent policy (http://www.ieee802.org/3/patent.html) and asked if anyone was unfamiliar with it. No one responded.

Andre asked if anyone had any objection or additions to the draft agenda. No one responded, so these agenda was approved by the Ad Hoc.

Andre asked if anyone had any corrections to the draft minutes from the 30 November 2015 call. No one responded, so these minutes are approved by the Ad Hoc.

Presentation #1

Title: Electrical Interface Ad Hoc Opening/Agenda

Presenter: Andre Szczepanek, Inphi

http://www.ieee802.org/3/bs/public/adhoc/elect/07Dec 15/szczepanek 01 120715 elect.pdf

Presentation #2

Title: CHANNEL OPERATING MARGIN (COM) PROPOSAL FOR CDAUI-8 C2C

Presenter: Magesh Valliappan, Broadcom

http://www.ieee802.org/3/bs/public/adhoc/elect/07Dec 15/valliappan 01 120715 elect.pdf

There was discussion on determining the Va, Vb, Vc, & Vd values from average levels in a PRBSQ13 eye diagram rather than from the TX linearity test pattern. Mike Dudek disagreed with this and requested the opportunity to show a slide to support his position. The presentation continued, and there was a positive reaction to the use of a least square method with PRBSQ13 data to estimate ES1 & ES2. Mike then presented a slide showing asymmetric PAM4 ISI at the inner (1,2) levels. It was discussed. A more detailed presentation on the least square method was requested. A discussion started on how "Receive level separation mismatch" is handled in COM. At 9:10am Andre call time on the discussion to allow Presentation #3 to be heard.

Presentation #3

Title: Channel Operating Margin (COM) proposal for CDAUI-8 C2C (Updated)

Presenter: Raj Hegde, Broadcom

http://www.ieee802.org/3/bs/public/adhoc/elect/07Dec 15/hegde 01 120715 elect.pdf

There was discussion about how Symbol Error rate can be measured for CDAUI-8 Receiver Interference tolerance Tests given 2 FEC lanes are bit multiplexed onto each lane. There was some support for a 1e-5 BER based on an "a=0.5" assumption.

At 9:50am, by way of closing business, Andre showed the results of the CRU Bandwidth Straw Poll taken by email (see presentation #1), but postponed the slides giving his observation of the CRU Bandwidth issue until the next meeting.

Andre asked if there was any objection to adding a Monday 21st December Ad Hoc call. There was only one objection. Andre will schedule a call and the meeting can be cancelled if there no presentation requests are made.

The meeting closed at 9:57 am Pacific.

Attendee list (taken from Webex attendee list)

Andre Szczepanek inphi Mike Dudek qlogic Kenneth Jackson sei-device Paul Mooney spirent Raj Hegde broadcom Adam Healey avagotech Yasuo Hidaka fujitsu **APIC** Randy k Rannow **Derek Cassidy** bt Ben Smith inphi sam sambasivan labs.att

Megha Shanbhag tyco electronics Rick Rabinovich alcatel-lucent

David Malicoat hpe
Jim Nadolny samtec
Piers Dawe mellanox
Adee Ran intel

Rick Rabinovich alcatel-lucent

Peter Anslow ciena

John Ewen globalfoundries

Qing Xu belden Upen Reddy cisco

martin white caviumnetworks

Ed Frlan semtech magesh broadcom Rick Rabinovich alcatel-lucent

Tomer Osi leabasemiconductor

Stephane Dallaire inphi Rich Mellitz intel Qian Zhang cisco

Brian Teipen advaoptical

Galen Fromm cray Phil Sun marvell **Tony Zortea** multi-phy Rita Horner synopsys **Rob Stone** broadcom **Gary Nicholl** cisco Wheling Cheng ericsson Jacky Chang hpe Jeffery Maki juniper