



Electrical Interface Ad-hoc Meeting

IEEE P802.3bs 400Gb/s Ethernet Task Force
8 Sept 2014 Version r1

Opening

The stated charter is: Work on technical proposals related to the 400GbE Electrical Interfaces.

Any changes to the meeting minutes from the last meeting?

Participants, Patents, and Duty to Inform

All participants in this meeting have certain obligations under the IEEE-SA Patent Policy.

- **Participants [Note: Quoted text excerpted from IEEE-SA Standards Board Bylaws subclause 6.2]:**
 - **“Shall inform the IEEE (or cause the IEEE to be informed)” of the identity of each “holder of any potential Essential Patent Claims of which they are personally aware” if the claims are owned or controlled by the participant or the entity the participant is from, employed by, or otherwise represents**
 - **“Personal awareness” means that the participant “is personally aware that the holder may have a potential Essential Patent Claim,” even if the participant is not personally aware of the specific patents or patent claims**
 - **“Should inform the IEEE (or cause the IEEE to be informed)” of the identity of “any other holders of such potential Essential Patent Claims” (that is, third parties that are not affiliated with the participant, with the participant’s employer, or with anyone else that the participant is from or otherwise represents)**
- **The above does not apply if the patent claim is already the subject of an Accepted Letter of Assurance that applies to the proposed standard(s) under consideration by this group**
- **Early identification of holders of potential Essential Patent Claims is strongly encouraged**
- **No duty to perform a patent search**

Patent Related Links

All participants should be familiar with their obligations under the IEEE-SA Policies & Procedures for standards development.

Patent Policy is stated in these sources:

IEEE-SA Standards Boards Bylaws

<http://standards.ieee.org/develop/policies/bylaws/sect6-7.html#6>

IEEE-SA Standards Board Operations Manual

<http://standards.ieee.org/develop/policies/opman/sect6.html#6.3>

Material about the patent policy is available at

<http://standards.ieee.org/about/sasb/patcom/materials.html>

If you have questions, contact the IEEE-SA Standards Board Patent Committee Administrator at patcom@ieee.org or visit <http://standards.ieee.org/about/sasb/patcom/index.html>

This slide set is available at <https://development.standards.ieee.org/myproject/Public/mytools/mob/slideset.ppt>

IEEE P802.3bs 400Gb/s Electrical Interface Ad-hoc – Sept 2014 Kanata

Call for Potentially Essential Patents

- If anyone in this meeting is personally aware of the holder of any patent claims that are potentially essential to implementation of the proposed standard(s) under consideration by this group and that are not already the subject of an Accepted Letter of Assurance:
 - Either speak up now or
 - Provide the chair of this group with the identity of the holder(s) of any and all such claims as soon as possible or
 - Cause an LOA to be submitted

Other Guidelines for IEEE WG Meetings

- **All IEEE-SA standards meetings shall be conducted in compliance with all applicable laws, including antitrust and competition laws.**
 - **Don't discuss the interpretation, validity, or essentiality of patents/patent claims.**
 - **Don't discuss specific license rates, terms, or conditions.**
 - Relative costs, including licensing costs of essential patent claims, of different technical approaches may be discussed in standards development meetings.
 - Technical considerations remain primary focus
 - **Don't discuss or engage in the fixing of product prices, allocation of customers, or division of sales markets.**
 - **Don't discuss the status or substance of ongoing or threatened litigation.**
 - **Don't be silent if inappropriate topics are discussed ... do formally object.**

See *IEEE-SA Standards Board Operations Manual*, clause 5.3.10 and “Promoting Competition and Innovation: What You Need to Know about the IEEE Standards Association’s Antitrust and Competition Policy” for more details.

Quick Note: This is a Consensus Building Meeting

IEEE P802.3bs Electrical Interface Ad-hoc

Build Consensus towards a presentation or adoption covering interface type, rate, modulation, and loss

Offer lots of constructive comments to converge quickly

Please mute your phone

Request Presentation Time

There are some things discussed that are outside the scope of the project, but are necessary to complete the system level discussion and drive the best Electrical Interface possible. Please clearly identify those parts in your discussions and contributions.

The Process Steps

The following simplified process steps are used to build consensus:

- Defining / Discussing Reach
- System Architecture
- Channel Loss – the first crack at it because one needs an egg to get to the chicken
- Modulation
- Equalization
- Error Correction
- Power

Agenda

Time	Presenter	Affiliation	Title	File Name
7:30 PM	Joel / Vasu		Agenda and General Information	goergen_3bs_elec_adhoc_01_0914.pdf
7:40 PM	John D'Ambrisia	Dell	Architecture and Electrical Interfaces	dambrosia_01_0914_elec.pdf
8:00 PM	Mike Li	Altera	Specification Considerations for CDAUI-16	??
8:20 PM	Joel Goergen	Cisco	Filling in a chart	goergen_3bs_elec_adhoc_02_0914.pdf
8:40 PM	Discussion of the OIF Response			



Thank you!

From:

Vasu Parthasarathy

Beth Kochuparambil

Vivek Telang

Joel Goergen