IEEE P802.3bs 400 Gb/s Ethernet Task Force Logic Ad Hoc

December 2nd, 2014

Mark Gustlin   - Xilinx

Meeting Notes

- The stated charter is: **Work on technical proposals related to the 400GbE Architecture**

- **Any changes to the meeting minutes from the 10/21/14 call?**

- **Reminder of the patent policy: http://www.ieee802.org/3/patent.html**
  - Is anyone unfamiliar with it?

- **Logic ad hoc location:**
  http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml

Agenda

> **Simplified Transcoding Scheme –** Zhongfeng Wang

> **Further analysis for distributed MLC for 400GE -** Zhongfeng Wang

> **Investigation on Technical Feasibility of Stronger RS FEC for 400GbE –** Xinyuan Wang

> Next Logic ad hoc call:
  – Tuesday December 16th, 8am PST