Unapproved minutes

IEEE P802.3bs 400 Gb/s Ethernet Task Force Logic Ad Hoc

Teleconference December 2nd, 2014

Minutes taken by Mark Gustlin, Xilinx
The meeting started at 8:01 am Pacific chaired by Mark Gustlin, the attendee list was taken from the WebEx attendee list.

Documentation for the call can be found at the Ad Hoc web page:
http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml

Mark showed the patent link and asked if anyone had any questions, no one responded.

Presentation #1
Title: Simplified Transcoding Scheme - Zhongfeng Wang
See: wangz_01_1214_logic.pdf
A lot of discussion around the fact that it is an interesting simplification, but it would need to be shown that it simplifies a 400/4x100G system overall, rather than just adding complication due to the deltas. Also need to quantify the savings.

Presentation #2
Title: Further analysis for distributed MLC for 400GE - Zhongfeng Wang
See: wangz_01_1214_logic.pdf
It was asked if this can be used for NRZ, no.
It was clarified that you don't need to correct the initial FEC, you just add the second MLC code to some bits. Was also asked what else needs to be done (alignment etc), that level of detail has not been looked at yet.
Also suggested that some slides could be simplified and just talk about the proposed solution, and not focus on the architectural aspects so much.

Presentation #3
Title: Investigation on Technical Feasibility of Stronger RS FEC for 400GbE – Xinyuan Wang et al.
See: wangx_01_1214_logic.pdf
It was asked if the latency is added or total? Added for the FEC only.
It was asked if the latency is ASIC based, no, it is FPGA based, might be useful to have both listed?
Slide 7, was asked why there were 20 AMs listed, for 400G we might use 16. This will be changed.
Could make it clearer on which FECs are 100G vs. 400G native.

Attendees (taken from webex, please let me know if you have a correction or addition):
Gregor Stellpfulg, Fujitsu
Matt Brown, Applied Micro
Rick Rabinovich, Alcatel-Lucent
Skabe Itaru, Sumitomo

Xinyuan Wang, Huawei
Mark Gustlin, Xilinx
David Yeh, Broadcom
Andy Moorwood, Ericsson
Keith Conroy, Multi-Phy
Pi Boson, ?
Tom Issenhuth, Microsoft
Steve Trowbridge, Alcatel-Lucent
Skabe Itaru, Sumitomo
omerzi?, Mellanox
Tongtong Wang, Huawei
Pirooz Tooyserkani, Cisco
Andre Szczepanek, Inphi
Paul Mooney, Spirent
Rakesh Sambaraju, Nexans
Pete Anslow, Ciena
Slobodan Milijevic, Microsemi
Gary Nicholl, Cisco
Kenneth Jackson, Sumitomo
Will Bliss, Broadcom
Tom McDermott, Fujitsu
Raymond Nering, Cisco
Mark Gravel, HP
Salvatore Rotolo, ST Microelectronics
Martin Bouda, Fujitsu
Martin Langhammer, Altera
Scott Irwin, MoSys Inc
Brian Teipen, Adva
Jeff Slavick, Avago Technologies
Bill Wilkie, Xilinx
Adam Healey, Avago Technologies
Brian Holden,
Piers Dawe, Mellanox
Jeffery Maki, Juniper
Ali Ghiasi, Independent
Rich Mellitz, Intel
Alex Umnov, Fujitsu
Robert Wang, Intel
omerzi?, Mellanox
Oded Wertheim, Mellanox
Peter Stassar, Huawei
Rob Stone, Broadcom

Zhongfeng Wang, Broadcom
Robert Coenen, Intel
Paul Kolesar, Commscope
Flavio Marques, Furukawa
Vasu Parthasarathy, Broadcom
Mike Li, Altera
John Ewen, IBM
Wheling Cheng, Ericsson
David Estes, Spirent
Rita Horner, Synopsys
John D'Ambrosia, Dell
Ali Ghiasi, Independent
Derek Cassidy, BT
Mike Dudek, Qlogic
Brian Welch, Luxtera
David Ofelt, Juniper
Benoit Mercier, ST Microelectronics
Wheling Cheng, Ericsson
Jonathan King, Finisar