IEEE P802.3bs 400 Gb/s Ethernet Task Force Logic Ad Hoc

February 17th, 2015

Mark Gustlin   - Xilinx

Meeting Notes

- The stated charter is: **Work on technical proposals related to the 400GbE Architecture**

- **Any changes to the meeting minutes from the 12/02/14 call?**

- **Reminder of the patent policy: http://www.ieee802.org/3/patent.html**
  - Is anyone unfamiliar with it?

- **Logic ad hoc location:**
  http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml

Agenda

> **Logic Big Ticket Items –** Mark Gustlin

> **Proposal for 400GbE FEC Architecture –** Xinyuan Wang

> Next Logic ad hoc call:
> – TBD