IEEE P802.3bs 400 Gb/s Ethernet Task Force Logic Ad Hoc

Teleconference February 23rd, 2016

Minutes taken by Mark Gustlin, Xilinx
The meeting started at 8:02 am Pacific chaired by Mark Gustlin, the attendee list was taken from the WebEx attendee list.

Documentation for the call can be found at the Ad Hoc web page:
http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml

Mark showed the patent link and asked if anyone had any questions, no one responded.

Presentation #1
Proposed MAC and PCS sublayer delay – Mark Gustlin et al.

Presentation #2
AMP_Valid and PCS_Lane TBDs – Mark Gustlin

Presentation #3
Skew in 400GbE – Mark Gustlin

Presentation #4
Alignment Marker Format Updates – Adrian Butter

Presentation #4
400GbE AMs revised proposal – Pete Anslow

Attendees (taken from webex, please let me know if you have a correction or addition):
  Mark Gustlin, Xilinx
  Thananya Baldwin, Ixia
  Jerry Pepper, Ixia
  Flavio Marques, Furukawa
  Andre Szczepanek, Inphi
  Will Bliss, Broadcom
  Jerry Pepper, Ixia
  Tongtong Wang, Huawei
  Pete Anslow, Ciena
  Jerry Pepper, Ixia
  Peter Stassar, Huawei
  Martin White, Cavium
  Adrian Butter, Global Foundries
  Ben Jones, Xilinx
  Tom McDermott, Fujitsu
  Piers Dawe, Mellanox
  Gary Nicholl, Cisco

Kenneth Jackson, Sumitomo
David Malicoat, HPE
Kumaran Krishnasamy, Broadcom
Eric Baden, Broadcom
John D'Ambrosia, Dell
John Dillard, Microsemi
Raymond Nering, Cisco
Mike Dudek, Qlogic
David Law, HPE
Geoffrey Chacon, HPE
Zvi Rechtman, Melanox
Martin Langhammer, Altera
Daniel Koehler, MorethanIP
Phil Sun, Credo Semiconductor
Tom Palkert, Xilinx, Luxtera, Molex
Hong Lim, Cisco
Mark Kimber, Semtech
Upen Reddy, Cisco
Rita Horner, Synopsys
Dave Ofelt, Juniper
Tom Mitcheltree, US Conec
Rick Rabinovich, Ixia
Zhenyu Liu, Credo Semiconductor
Mark Gravel, HP
Wheling Cheng, Ericsson
Ali Ghiasi, Independent
JC Calderon, Inphi
Q. Zhang, Cisco
Ed Frlan , Semtech
Gary Lim, Charter
David Ofelt, Juniper
Bill Wilkie, Xilinx
Jeffery Maki, Juniper