

IEEE P802.3bs 200 Gb/s and 400 Gb/s Ethernet Task Force Logic Ad Hoc

Teleconference January 26th, 2017

Minutes taken by Mark Gustlin, Xilinx

The meeting started at 7:32 am Pacific chaired by Mark Gustlin, the attendee list was taken from the WebEx attendee list.

Documentation for the call can be found at the Ad Hoc web page:
<http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml>

Mark showed the patent link and asked if anyone had any questions, no one responded.
Mark also showed the participation requirement slide.

Presentation #1

Possible Solutions for Clock Content Issue: Mark Gustlin, Xilinx

Presentation #2

Restricted muxing option: Mark Gustlin - Xilinx, Dave Ofelt – Juniper, Gary Nicholl – Cisco, Kapil Shrikhande - Innovium

Presentation #3

On clock content issue over four lane interleaving: Ryan Wong, Eric Baden, Rob Stone - Broadcom

Attendees (taken from webex, please let me know if you have a correction or addition):

Mark Gustlin, Xilinx

John D'Ambrosia, Dell

Daniel Koehler, MorethanIP

Mark Kimber, Semtech

Tom McDermott, Fujitsu

Xinyuan Wang, Huawei

Rick Rabinovich, Ixia

Steve Trowbridge, Nokia

Mike Dudek, Cavium

Andre Szczepanek, Inphi

Paul Kolesar, Commscope

Ryan Wong, Broadcom

Salvatore Rotolo, ST

Microelectronics

Bill Kirkland, Semtech

Pete Anslow, Ciena

Don Cober, Comira

David Estes, Spirent

Michael Ressler, Hitachi Cable

Jim Duda, Acacia Communications

Derek Cassidy, BT
Adrian Butter, Global Foundries
David Law, HP
Kapil Shrikhande, Innovium
Martin White, Cavium
Stephen Didde, Keysight
Oded Wertheim, Mellanox
Ben Jones, Xilinx
Gary Nicholl, Cisco
Marco Mazzini, Cisco
Ali Ghiasi, Huawei
Piers Dawe, Mellanox
David Lewis, Lumentum
Zvi Rechtman, Mellanox
Phil Sun, Credo Semiconductor
Ted Sprague, Infinera
John Dillard, Microsemi
Peter Stassar, Huawei
David Ofelt, Juniper
Weiyang Cheng, Coriant
John Ewen, Global Foundries
Sam Sambasivan, AT&T
Raj Hegde, Broadcom
Hanan Leizerovich, Multi-Phy
Karen Liu, Kaiam
Jeffery Maki, Juniper
Slobodan Milijevic, Microsemi