IEEE P802.3bs 400 Gb/s Ethernet Task Force Logic Ad Hoc

June 19, 2015

Mark Gustlin - Xilinx

Meeting Notes

- ➤ The stated charter is: Work on technical proposals related to the 400GbE Architecture
- ➤ Any changes to the meeting minutes from the 4/26/15 call?
- > Reminder of the patent policy: http://www.ieee802.org/3/patent.html
 - Is anyone unfamiliar with it?
- ➤ Attendees names and affiliations will be taken from the Webex participants list. If you attend via phone only, or if your employer and affiliation are different, please send me an e-mail.
- > Logic ad hoc location:

http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml

Agenda

- ▶ 400GE FEC Implementation Martin Langhammer
- Considerations for breakout Martin Langhammer
- ➤ 1x400G vs 4x100G FEC Implications Bill Wilkie
- ➤ Analysis on 400G FEC Architecture Phil Sun
- ➤ Next Logic ad hoc call:
 - June 29th, 8am PDT