## IEEE P802.3bs 400 Gb/s Ethernet Task Force Logic Ad Hoc

Teleconference June 29th, 2015

Minutes taken by Mark Gustlin, Xilinx The meeting started at 8:02 am Pacific chaired by Mark Gustlin, the attendee list was taken from the WebEx attendee list.

Documentation for the call can be found at the Ad Hoc web page: http://www.ieee802.org/3/bs/public/adhoc/logic/index.shtml

Mark showed the patent link and asked if anyone had any questions, no one responded.

Presentation #1 Title: 400GE FEC Implementation - Tongtong Wang, Xinyuan Wang See: wangx\_01\_0615\_logic.pdf

Presentation #2 Title: 400GbE FEC Discussion- Tongtong Wang, Xinyuan Wang See: wangt\_01\_0615\_logic.pdf

Presentation #3 Title: Further Analyses of Reducing Complexity for Data Alignment – Zhongfeng Wang See: wangz\_01\_0615\_logic.pdf

Attendees (taken from webex, please let me know if you have a correction or addition): Mark Gustlin, Xilinx Tom McDermott, Fujitsu Paul Mooney, Spirent Xinyuan Wang, Huawei omerzi?, Mellanox Ali Ghiasi, Independent Adrian Butter, IBM Rick Rabinovich, Alcatel-Lucent Phil Sun, Marvell David Ofelt, Juniper Eric Fortin, ALU Pete Anslow, Ciena Zhongfeng Wang, Broadcom Pi Boson, ? Zvi Rechtman, Melanox John D'Ambrosia, Dell Eric Baden, Broadcom Tongtong Wang, Huawei

S.K. Shah, Broadcom Derek Cassidy, BT Yuri Vandyshev, Cisco Martin Langhammer, Altera Mike Dudek, Qlogic Brian Teipen, Adva Steve Trowbridge, Alcatel-Lucent David Malicoat, HP Bill Wilkie, Xilinx Kenneth Jackson, Sumitomo Peter Stassar, Huawei Daniel Koehler, MorethanIP Jeff Slavick, Avago Technologies John Ewen, IBM Piers Dawe, Mellanox Martin White, Cavium Scott Irwin, MoSys Inc Matt Brown, Applied Micro Rakesh Sambaraju, Nexans Raymond Nering, Cisco Mike Dudek, Qlogic Michael Ressl, Hitachi Cable Mike Li, Altera Peter Stassar, Huawei Mark Gravel, HP JC Calderon, Inphi Ali Ghiasi, Independent Wheling Cheng, Ericsson Mark Nowell, Cisco Oded Wertheim, Mellanox Nigel Gulstone, Altera