Restricted muxing update

Logic Ad Hoc call, March 9th

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Restricted muxing initial solution

- Restricted muxing as a potential fix to clock content issue presented
 - PMA 16:8 to mux natural pair of PCSLs on to 50G lanes
 - PMA 8:4 to mux any two sets of natural pairs of PCSLs on to 100G lanes
 - Avoids rogue combinations on 100G lanes
 - Blind demux on 4:8 and 8:16 since we've avoided issue on 100G lanes
- But was pointed out that assuming 802.3bs PCS/FEC can work for 100G per lane AUIs, the above does not cover cases like 400GAUI-4 + 4:8 + 8:4 PMA
 - 4:8 PMA not guaranteed to maintain natural pairs on 50G output lanes
 - Subsequent 8:4 PMA could lead to a rogue PCSL combination on 100G lanes

Restricted muxing updated with options A, B

- Additional rules for 8:4 and 4:8 PMA mux that preserves natural pair of PCSLs on 50G lanes presented, that avoid rogue combinations through 4:8 + 8:4 PMA chain
 - Two options A and B were presented, as follows
 - Option B was seen as a specific way to do PMA bit-muxing seemed to be favored
 - Option A: Keep natural pairs together in each PAM4 symbol: 0+1 in one PAM4 symbol, 2+3 in 2nd PAM4 symbol, then back to 0+1



• Option B: Bit mux the natural pairs with each other: 0+2 in one PAM4 symbol, or 1+3 in the next PAM4 symbol (0+3 and 1+2)



& undo bit muxing in the 1:2 demux

Restricted muxing proposal

- PMA 16:8 rule as before
- PMA 8:4 rule (option B)
- PMA 4:8 rule (option B)
- PMA 16:4 is a combination of 16:8 + 8:4 and follows rules of both
- PMA 8:16 no rules (but ...)

(Content of submission to 802.3bs interim meeting in Vancouver)

Revisit the 8:16 PMA rule

- No rule associated with PMA 8:16? OK if followed by PCS Rx
- But was pointed out that if PMA 8:16 is followed by another PMA 16:8, e.g.
 400GAUI-8 + PMA 8:16 + 400G-DR4 module with 16:4 PMA inside the module ?
- To solve the above natural pair of PCSLs should be mapped to a natural pair of 26.5625G PMA lanes
 "Natural Pairs"

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Restricted muxing proposal - revised

- PMA 16:8 rule as before
- PMA 8:4 rule (option B)
- PMA 4:8 rule (option B)
- PMA 16:4 is a combination of 16:8 + 8:4 and follows rules of both
- PMA 8:16 rule
 - Natural pair of PCSLs to fall on natural pair of 26.5625 Gb/s PMA lanes

Restricted muxing described as invariant conditions

- Conditions that are held invariant across every multiplexing stage
 - 1. Every natural pair of physical lanes operating at 26.5625 Gb/s carries a natural pair of PCSLs. Not necessarily the same pair, and not necessarily in the same order. For example, physical lanes 9,10 could carry PCSLs 4,3.
 - 2. Every physical lane operating at 53.125 Gb/s carries a natural pair of PCSLs, with one PCSL encoded as the A bit of each PAM4 symbol and the other PCSL encoded as the B bit of each PAM4 symbol.
 - 3. Every physical lane operating at 106.25 Gb/s carries two natural pairs of PCSLs, with one natural pair encoded on the A bits of two consecutive PAM4 symbols, and the other natural pair encoded on the B bits of two consecutive PAM4 symbols.

Alternate Option: 100G Slices

- Stay with the concept of generic bit muxes, but keep 100G slices together
- A 400G 16:8 mux consists of four generic 4:2 muxes from four consecutive input lanes to two consecutive output lanes. A 16:4 mux is four 4:1 muxes. Similar for 200G, where it is partitioned into two 100G slices (not using that terminology in the draft).
- Net effect is that any 100G lane is composed of four consecutive PCSLs in some order, and none of these are pathologically low clock content options.

Next steps

- Revise restricted muxing proposal in time for Vancouver
 - Either continue to specify as series of rules for each PMA m:n mux type
 - Or, adopt the elegant approach to describe the conditions that are held invariant across each multiplexing stage
- If group is interested and there's some consensus to pursue it, develop alternate proposal using 100G slices
 - Different set of pros/cons compared to existing restricted muxing proposal