## Approved minutes P802.3bs 400 Gb/s Ethernet SMF Ad Hoc Teleconference 15 December 2015

Minutes taken by Pete Anslow, Ciena

The meeting started at 8:02 am Pacific chaired by Pete Anslow, the attendee list was taken from the Webex attendee list plus any e-mail notifications of attendance.

Documentation for the call can be found at the Ad Hoc web page: <a href="http://www.ieee802.org/3/bs/public/adhoc/smf/index.shtml">http://www.ieee802.org/3/bs/public/adhoc/smf/index.shtml</a>

Pete reminded everyone of the updated IEEE patent policy (<a href="http://www.ieee802.org/3/patent.html">http://www.ieee802.org/3/patent.html</a>) and asked if anyone was unfamiliar with it. No one responded.

Pete asked if anyone had any objection or additions to the draft agenda. No one responded, so these agenda was approved by the Ad Hoc.

Pete asked if anyone had any corrections to the draft minutes from the 1 December 2015 call. No one responded, so these minutes are approved by the Ad Hoc.

## Presentation #1

Title: 802.3bs: Proposal for PAM4 PMDs link budgets harmonization

Presenter: Marco Mazzini, Cisco See mazzini\_01\_1215\_smf

During the presentation of mazzini\_01\_1215\_smf, there was a discussion as to whether 4.8 dB was the appropriate value to offset the transmitter OMA<sub>outer</sub> by to derive the receiver sensitivity in OMA<sub>inner</sub>. The consensus view was that it was. Concerns were raised as to whether 55 dB return loss and 0.15 dB MPI penalty was an appropriate tradeoff between penalty and robustness to reflections, with 45 dB return loss and 0.2 dB MPI penalty being proposed as more appropriate. Also, it was noted that there was an error on page 9 (0.25 dB should have been 0.15 dB). A revised version of mazzini\_01\_1215\_smf was posted after the meeting to correct this.

## Presentation #2

Title: 400GBASE-DR4 PMD Considerations for TX OMA reduction

Presenter: Marco Mazzini, Cisco See mazzini\_02\_1215\_smf During the presentation of mazzini\_02\_1215\_smf, Marco was asked what test pattern was used. The response was that the curves were for PRBS15. Pete expressed the view that this pattern is not sufficiently stressful and Marco responded that the results were the same for SSPR. However, it then emerged that the pattern used was two SSPR patterns offset by several bits (one SSPR for MSB and the other for LSB).

Pete asked whether there was a need for another SMF Ad Hoc meeting and it was agreed to schedule an additional meeting for Thursday 7 January at 8:00 am Pacific.

The meeting closed at 9:44 am Pacific.

Attendee list (taken from Webex attendee list)

Pete Anslow, Ciena Paul Mooney, Spirent Vipul Bhatt, Inphi Gary Nicholl, Cisco

William Bliss, Broadcom Rick Pimpinella, Panduit

Derek Cassidy, BT (Affiliation ICRG) R K Rannow, APIC

Wheling Cheng, Ericsson Salvatore Rotolo, STMicroelectronics

John D'Ambrosia, Independent Scott Schube, Intel

Piers Dawe, Mellanox Dan Sparacin, Aurrion Mike Dudek, Qlogic Peter Stassar, Huawei

Ali Ghiasi, Ghiasi Quantum LLC Phil Sun, Marvell

Mark Kimber, Semtech

Jonathan King, Finisar

Keisuke Kojima, Mitsubishi

Andre Szczepanek, Inphi
Bharat Tailor, Semtech
Matt Traverso, Cisco

Greg LeCheminant, Keysight Stephen Trowbridge, Alcatel-Lucent

David Lewis, Lumentum Ed Ulrichs, Source Photonics

Hai Fong Liu, Intel

Chuanhiao Wang Hisilicon

Hai-Feng Liu, Intel Chuanbiao Wang, HiSilicon
David Malicoat, HP Enterprise Brian Welch, Luxtera

Marco Mazzini, Cisco Martin White, Cavium

Thomas McDermott, Fujitsu Pavel Zivny, Tektronix