Approved minutes P802.3bs 400 Gb/s Ethernet SMF Ad Hoc Teleconference 2 February 2016

Minutes taken by Pete Anslow, Ciena

The meeting started at 8:04 am Pacific chaired by Pete Anslow, the attendee list was taken from the Webex attendee list plus any e-mail notifications of attendance.

Documentation for the call can be found at the Ad Hoc web page: http://www.ieee802.org/3/bs/public/adhoc/smf/index.shtml

Pete reminded everyone of the IEEE patent policy (http://www.ieee802.org/3/patent.html) and asked if anyone was unfamiliar with it. No one responded.

Pete asked if anyone had any objection or additions to the draft agenda. John D'Ambrosia asked if he could add an item on the project schedule. With this addition, the agenda was approved by the Ad Hoc.

Pete asked if anyone had any corrections to the draft minutes from the 7 January 2016 call. No one responded, so these minutes were approved by the Ad Hoc.

John D'Ambrosia (P802.3bs Chair) reiterated the importance of completing as soon as possible the work to address all of the remaining open issues in relation to the SMF optical PMDs to ensure progress towards a technically complete draft for 400 Gb/s Ethernet.

Presentation #1

Title: SMF TBDs

Presenter: Pete Anslow, Ciena

See anslow_01_0216_smf and anslow_02_0216_smf

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Regarding the reflection budget straw man proposal for 400GBASE-DR4, Pete was asked what the basis for the 0.6 dB penalty was. Pete responded that it was the result from Jonathan King's spreadsheet king 02a 0116 smf with six -35 dB connectors and four -55 dB connectors. A chart showing this result was shown and this was added to an updated version (anslow_01a_0216_smf) of the presentation posted after the close of the meeting. As there are only four high reflection connectors in the double link scenario in kolesar 3bs 01 0514, Pete said he would re-run the spreadsheet with four -35 dB connectors and four -55 dB connectors.

Pages 5 and 6 (pages 6 and 7 in anslow 01a 0216 smf)

Input is needed on how to modify the optical budgets for 400GBASE-FR8 and 400GBASE-LR4 to accommodate the 0.5 dB and 0.6 dB MPI penalty, respectively.

Page 7 (page 8 in anslow_01a_0216_smf)

There was no disagreement with the proposal to change the delay constraint values black.

Page 9 (page 10 in anslow 01a 0216 smf)

The consensus view was to base the OMAouter and ER definitions on the PRBS13Q sequence. The zero level was proposed to be the average of the central 2 unit intervals (symbols 3 and 4) of the run of 6 zeros and the three level was proposed to be the average of symbols 4 and 5 of the run of 7 threes.

Page 12 (page 13 in anslow_01a_0216_smf)

For the maximum mean DGD of the test channel for the PAM4 TDEC test, the penalty due to 2.24 ps of DGD was requested.

Pete reminded the meeting that there will be two more SMF Ad Hoc meeting opportunities on Tuesday 16 February at 9:00 am Pacific and Tuesday 1 March at 8:00 am Pacific.

The meeting closed at 10:06 am Pacific.

Attendee list (taken from Webex attendee list)

Ghani Abbas, Ericsson

Pete Anslow, Ciena Hanan Leizerovich, MultiPhy

Vipul Bhatt, Inphi Hai-Feng Liu, Intel

William Bliss, Broadcom David Malicoat, HP Enterprise

Derek Cassidy, BT (Affiliation ICRG) Marco Mazzini, Cisco

Geoffrey Chacon, HP Enterprise Thomas McDermott, Fujitsu

Wheling Cheng, Ericsson Gary Nicholl, Cisco

Chris Cole, Finisar Rick Pimpinella, Panduit

John D'Ambrosia, Futurewei (a subsidiary Rick Rabinovich, Ixia

of Huawei) R K Rannow, APIC

Piers Dawe, Mellanox Peter Stassar, Huawei

Mike Dudek, Qlogic Andre Szczepanek, Inphi
Ali Ghiasi, Ghiasi Quantum LLC Bharat Tailor, Semtech

Rita Horner, Synopsys Frederick Tang, Broadcom

Kenneth Jackson, Sumitomo Nathan Tracy, TE Connectivity

Mark Kimber, Semtech

Jonathan King, Finisar

Keisuke Kojima, Mitsubishi

Reddy Upen, Cisco

Or Vidal, MultiPhy

Martin White, Cavium

Paul Kolesar, CommScope Pavel Zivny, Tektronix Greg LeCheminant, Keysight xhuang163, Unknown