Inrush text v100

145.2.5.7 State diagrams

Change the contents of the POWER_UP state in Figure 145–13 to read:

```
alt_pwrd_pri <= TRUE
start tinrush_timer_pri
IF ((pse_alternative=both) * (pse_ss_mode=1)) + (pse_allocated_pwr>6) THEN
   alt_pwrd_sec <= TRUE
   start tinrush_timer_sec
END
```

145.2.8.6 Output current during power up

Power up occurs on each pairset between the transition to a power up state on that pairset and the expiration of T_{Inrush}. ~~PSEs that have assigned Class 5 to 8 to a single-signature PD shall reach POWER_UP on both pairsets within T_{Inrush} max, starting with the first pairset transitioning into the power up state, and where the second pairset transitions to a power up state anytime within this time period.~~ A PSE that provides current on both pairsets during POWER_UP shall complete power up within T_{Inrush} max, starting when the first pairset exceeds a voltage of 30 V. PSEs that have assigned Class 1 through 6 to a single-signature PD may perform inrush over 2 pairs or over 4 pairs. PSEs that have assigned Class 5 or Class 6 to a single-signature PD transition to 4-pair mode by T_{Inrush}. PSEs that have assigned Class 7 or Class 8 to a single-signature PD perform inrush over 4 pairs.