

IEEE802.3bt  
LLDP adhoc report  
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# List of attendees

Name	Employed by:	Affiliated with:	Present in meeting #				
			1	2	3	4	5
<b>Bruce Nordman</b>	IBL	LBNL	Y				
<b>Chad Jones</b>	Cisco	Cisco	Y	Y	Y	Y	Y
<b>Chris Bullock</b>	Cisco	Cisco					Y
<b>David Tremblay</b>	HPE	HPE	Y	Y	Y		
<b>Geoff Thompson</b>	Unemployed	Unaffiliated	Y	Y	Y		Y
<b>Heath Stewart</b>	ADI/LT	ADI/LT	Y				Y
<b>John Skinner</b>	Sifos	Sifos	Y	Y	Y	Y	
<b>Yair Darshan</b>	Microsemi	Microsemi	Y	Y	Y	Y	Y
<b>David Law</b>	HPE	HPE		Y	Y	Y	Y
<b>Murat Karaorman</b>	ADI/LT	ADI		Y	Y		
<b>David Stover</b>	ADI/LT	ADI		Y	Y		Y
<b>Lennart Yseboodt</b>	Philips	Philips		Y	Y	Y	Y
<b>Arkadiy Peker</b>	Microsemi	Microsemi					Y
<b>David Abramson</b>	TI	TI					Y
<b>Andrea Agnes</b>	ST	ST					Y
<b>Joab Lemahica</b>	ON	ON					Y
<b>Dylan Walker</b>	Cisco	Cisco					Y
<b>Mathias Wendt</b>	Philips	Philips					Y
<b>Fred Dawson</b>	Cherouzs	Cherouzs					Y



# Topics Discussed and status

#	Subject	Status	Reference in the adhoc material
1	TLVs content as function of PSE / PD / operating over 2P or 4P	Done	Table 1
2	PD DLL state machine changes to resolve comment #297 D2.4	Done	Annex B
3	To use total available power in the field “PSE maximum available power” in 79.3.2.6e instead of $Y=A+B$ in the PSE for both single signature and dual signature.	Done. To update DLL baseline	See proposed baseline and Annex B.
4	Transition from 4P to 2P and back to 4P concept	Done	Table 1
5	To add text to 145.5.4.X to explain how the PSE and PD get into <b>sync</b> when they transition from 2P to 4P and back.	On going	Meeting #4 adhoc material
6	In the transition from 4-pair to 2-pair the minimum value that goes to Y is the last allocated value of A or B which ever stays active. (to be added to the description text of state machine and not to the state machine). Update: The PD can set any new power value in Y.	Done	Meeting #3 adhoc material. See updates in LLDP baseline for meeting #5.
7	Comment #130 to replace “shall” with “out of scope”. To move 4PID to legacy field.	Done	Meeting #5 adhoc material
8	To update DLL state machine to sync with concept table	On going	See meeting #5 discussions.



# Points of agreements from last meetings

- Meeting #2: Group agrees that the concept tables reflect D2.5 spec.
- Meeting #3: Group agrees to the concept table changes [marked with BLUE text](#).
- Meeting #3: Group agrees to the proposed changes to PD DLL state machine Figure 145-43. [See Annex B for details](#).
- Meeting #3: Group agrees to the proposed response to comment #297 D2.4. [See Annex B for details](#).
- Meeting #3: Group agrees to use total available power in the field “PSE maximum available power” in 79.3.2.6e instead of  $Y=A+B$  in the PSE for both single signature and dual signature. It will not be part of the state machine but it is available to the user as the rest of the new TLVs. [See Annex B for details and changes in the proposed baseline](#).
- Meeting #3: In the transition from 4-pair to 2-pair the minimum value that goes to Y is the last allocated value of A or B which ever stays active. (to be added to the description text of state machine and not to the state machine). [See concept table](#).
- Meeting #4: Group agrees with the concept Table. Lennart will confirm by Sunday July 9, 2017.
- Meeting #4: To add text to 145.5.4.X to explain how the PSE and PD get into **sync** when they transition from 2P to 4P and back.
- Meeting #5: [See next slide](#).



# Points of agreements from last meetings – Cont.

## ■ Meeting #5:

- To file MR to change the DLL state diagram in Clause 33, to restrict the value PSEAllocatedPowerValue from 1 through 255. Proposed change is not resulting in a changes to legacy requirements.
- Comment #130 from D2.4 resolution is resolve per the proposal in the LLDP baseline after changing the “shall” to “out of scope”

## ■ LLDP baseline:

- To delete from LLDP Baseline the proposed table to change from LLDP single-signature and dual-signature state machines to single-mode and dual mode operation. As a result we currently staying in the baseline with the concept of single-signature and dual-signature state machines.
- To change the text from “Type 1 and Type 2 devices shall not support the Type 3 and Type 4 extension” to “Type 3 and Type 4 extension are out of scope for Type 1 and Type 2 devices” or equivalent wording.
- To consider the changes to dll\_enable (it was dll\_enable\_mode/Alt(X)) in the proposed LLDP baseline. We had good reason for why did I tin D2.5.

