LLDP Adhoc
Meeting 9
August 22, 2017

Minutes by: Chad Jones

All times US EDT

11:08 Chair starts meeting and reviews agenda.
11:09 Chair covered patent policy.
11:11 Agenda approved
11:11 Chair reviews points of agreement and changes since last meeting.

12:04 new concept, from the start up of LLDP – determine if we are using the Y fields or the AB fields and that never changes until the port is powered down.

Target concept: A Type 3or 4 PSE connected to a dual signature PD uses the AB fields. All other cases use the Y field.
Corner case: Type 3 PSE 2P only (Class 3,4). How does a PD know that it is connected to a 2P PSE? There is no mechanism.
Suggestion: all DS PDs fill out both AB and Y. The problem lies with the DS PD, this puts the burden of the solution on the PD.

12:08 Agenda exhausted.

12:08 Adjourned.

Open Action Items:
- ~~Chris will check the need to keep pse_dll_ready_alt(X) or unified it. (We already decide to unify pd_dll_ready_mode(X) to pd_dll_ready to resolve comment #297 D2.4~~
- ~~Heath to propose a new SD concept that narrows the changes~~
- Yair and Lennart to update LLDP state machine to a single DLL enable

New Action Items:
- Chris: use Lennart's table to show Chris' use cases.

Name	Employed by:	Affliliated with:	Present:
Bruce Nordman	LBNL	LBNL	
Chad Jones	Cisco	Cisco	Y
Chris Bullock	Cisco	Cisco	Y
David Law	HPE	HPE	
David Stover	ADI/LT	ADI/LT	
David Tremblay	HPE	HPE	Y

Geoff Thompson	Unemployed	Unaffiliated	
Heath Stewart	ADI/LT	ADI/LT	Y
John Skinner	Sifos	Sifos	
Lennart Yseboodt	Philips	Philips	Y
Murat Karaorman	ADI/LT	ADI/LT	
Yair Darshan	Microsemi	Microsemi	Y