## PD Peak unbalance specification v110

## **Info** (not part of baseline)

Subclause 145.3.8.10 ("PD pair-to-pair current unbalance") specifies the maximum pair current for PDs under unbalance 4-pair operation. It only specifies continuous unbalance current. Peak unbalance requirements are not specified.

- For time durations shorter than T<sub>CUT-2P</sub> min, the PD must not exceed the peak unbalance current I<sub>Peak-2P-unb</sub>.
- The "under all operating states" is a redundant qualifier.
- The "all common source voltages" could be interpreted as "all of them at the same time" which is not what we mean.
- Now that we have added a spec for I<sub>Con-2P-unb</sub> for all Classes, we can remove "assigned to Class 5 or higher".
- I<sub>Con-2P</sub> is a PSE parameter, not usable in this section. It is replaced by the equivalent P<sub>Class\_PD-2P</sub>/V<sub>PD</sub>.

## 145.3.8.10 PD pair-to-pair current unbalance

Under all operating states, s Single-signature PDs assigned to Class 5 or higher shall not exceed  $I_{Con-2P-unb}$  for longer than  $T_{CUT-2P}$  min, and shall not exceed  $I_{Peak-2P-unb}$ , as defined in Table 145–16 on any pair when PD PI pairs of the same polarity are connected to all possible common source voltages any voltage in the range of  $V_{Port\_PSE-2P}$  through two common mode resistances,  $R_{source\_min}$  and  $R_{source\_max}$ , as defined in Equation (145–32) and shown in Figure 145–34.

Under all operating states, d Dual-signature PDs shall not exceed  $I_{Con-2P}$   $P_{Class\_PD-2P}/V_{PD}$  as defined in Equation (145–8) for longer than  $T_{CUT-2P}$  min, and shall not exceed  $P_{Peak\_PD-2P}/V_{PD}$ , as defined in Table 145–16 on any pair when PD PI pairs of the same polarity are connected to all possible common source voltage any voltage in the range of  $V_{Port\_PSE-2P}$  through two common mode resistances,  $R_{source\_min}$  and  $R_{source\_max}$ , as defined in Equation (145–32) and shown in Figure 145–34.