

# PSE Polarity Baseline v320

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Change the Table 33-2 in section 33.2.3

Conductor	Alternative A (MDI)	Alternative A (MDI-X)	Alternative B	Alternative B (X)
1	Positive $V_{PSE}$	Negative $V_{PSE}$		
2	Positive $V_{PSE}$	Negative $V_{PSE}$		
3	Negative $V_{PSE}$	Positive $V_{PSE}$		
4			Positive $V_{PSE}$	Negative $V_{PSE}$
5			Positive $V_{PSE}$	Negative $V_{PSE}$
6	Negative $V_{PSE}$	Positive $V_{PSE}$		
7			Negative $V_{PSE}$	Positive $V_{PSE}$
8			Negative $V_{PSE}$	Positive $V_{PSE}$

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Add new table to 33.2.3

Table 33-X – Permitted pinout alternatives per type

PSE Type	Alternative A (MDI)	Alternative A (MDI-X)	Alternative B	Alternative B (X)
Type 1,2	Yes	Yes	Yes	No
Type 3	Yes	Yes	Yes	Yes
Type 4	No	Yes	Yes	No

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## Change the following in section 33.2.3

PSEs that use automatically-configuring MDI/MDI-X (“Auto MDI-X”) ports may choose either polarity choice associated with Alternative A configurations.

### To read:

PSEs may choose the polarity choices associated with Alternative A or Alternative B listed in Table 33-X corresponding with their Type.

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Change the following in section 33.2.3

A PSE shall implement Alternative A, Alternative B, or both.

To read:

**Type 1, Type 2 or Type 3** PSEs shall implement Alternative A, Alternative B, or both.

**Type 4 PSEs** shall implement Alternative A and Alternative B.