4PID Ad hoc Report

IEEE 802.3: 4PPOE Task Force

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Ad Hoc Report

- Meeting held 12 May 2015, 10 attendees*
- 1 Presentation (one in reserve)
 4PID Concepts for Baseline Text
 - (zimmerman_3btah_01_0515.pdf)
 - Was updated during meeting to version 01a capturing start of discussion
- Vigorous and useful discussion on where we are, resulting in proposed baseline text: zimmerman_3btah_02a_0515.pdf

Discussion Summary

- From before: 4PID can be expressed logically as: (valid_detect_A)*(valid_detect_B)*(CC=Option 1) + (valid_detect_A)*(valid_detect_B)*(CC=Option 2)* [x?]
- Question was 'what is 'x' '?
 - Unconditionally TRUE, or something else?
- Vigorous discussion resulted in the following gaining consensus, adding a check to maintain 4P power:
 - 4PID physical layer (no LLDP) : x=TRUE , alternatively, x = !Deny_dual_sig_4P_Power.
 - Maintain_4P_power (beyond time TBD) = (4PID=TRUE) * (LLDP != FALSE) + (LLDP = TRUE) * [Class_power_OK]
 - NOTE: if Maintain_4P_power is false, then power must be removed from at least one pair set. x may be disabled by vendor-discretionary bit.