

Adding text that addresses the new 110uF value for dual-signature class 1-4.

### 33.3.7.6 PD behavior during transients at the PSE PI

Delete the following Editor Note:

~~Editor's Note: 1. Type 3 and Type 4 to be added (to parts other than the newly added first paragraph).~~

A PD shall continue to operate without interruption in the presence of transients at the PSE PI as defined in 33.2.8.2. A single-signature PD shall include  $C_{port}$  as defined in Table 33–28. A dual-signature PD shall include  $C_{Port-2P}$  as defined in Table 33–28 on each pairset.

A Type 1 PD with input capacitance of 180  $\mu\text{F}$  or less requires no special considerations with regard to transients at the PD PI. A Type 2 or single-signature Type 3 PD with peak power draw that does not exceed  $P_{Class\_PD\ max}$  and has an input capacitance of 180  $\mu\text{F}$  or less requires no special considerations with regard to transients at the PD PI. A single-signature Type 4 PD with peak power draw that does not exceed  $P_{Class\ PD\ max}$  and has an input capacitance of 360 $\mu\text{F}$  or less requires no special considerations with regards to transients at the PD PI.

A dual-signature Type 3 ~~or Type 4 PD~~ with peak power draw that does not exceed  $P_{Class\_PD-2P\ max}$  and has an input capacitance of 110 uF ~~180  $\mu\text{F}$~~  or less per pairset requires no special considerations with regard to transients at the PD PI. A dual-signature Type 4 with peak power draw that does not exceed  $P_{Class\ PD-2P\ max}$  and has an input capacitance of 180 uF or less per pairset requires no special considerations with regard to transients at the PD PI.

PDs that do not meet these requirements shall comply with the following:

No changes are required beyond this point in 33.3.7.6