

Comment #26 clause 33.2.10.1.2, page 119 line 22.

Update comment.

False disconnect or false maintain power as a result of short MPS under PSE transient [conditions](#) need to be addressed. We need to allow PSE system to decide what to do in this case when a PSE dv [from TBD to ~~of up to~~ 2V](#) for a dt of 0.8ms to 20ms which results [with distorted](#) of the short MPS pulse for at least one cycle of MPS+TMPDO for a specific time window.

Updated remedy.

Add the following text to the end of section 33.2.10.1.2:

Option 1:

Type 3 and Type 4 PSE when supporting short MPS may fail to detect presence or absence of a short MPS pulse as a result of PSE dv/dt that may cancel or distorted or add MPS pulse. Type 3 and Type 4 PSE when supporting short MPS during PSE dv/dt for PSE voltage change dv [from TBD\(\\*\) V ~~of up to~~ 2V](#) and time duration dt of 0.8msec to 10msec for a sliding time window of 3 sec (TBD) may maintain the power or disconnect the power when presence or absence of short MPS pulse is not possible [to detect](#) under the above conditions.

[\[\(\\*\) value will be supplied at the meeting if this option will be accepted.\]](#)

Option 2:

A PSE may ignore the current MPS status of a short MPS pulse once every 3 [\(TBD\)](#) seconds, which permits PSEs to deal with seldom occurring transients that may distort the MPS signal.