

79.5.3 Major capabilities/options

Item	Feature	Subclause	Value/Comment	Status	Support
*MP	MAC/PHY Configuration/Status TLV	79.3.1		O	Yes [] No []
*PV	Power Via MDI TLV	79.3.2		O	Yes [] No []
*LA	Link Aggregation TLV	79.3.3	TLV deprecated	O	Yes [] No []
*FS	Maximum Frame Size TLV	79.3.4		O	Yes [] No []
*EE	EEE TLV	79.3.5		O	Yes [] No []
*EEFW	EEE Fast Wake TLV	79.3.6		O	Yes [] No []

79.5.4 IEEE 802.3 Organizationally Specific TLV

Item	Feature	Subclause	Value/Comment	Status	Support
TLV1	Group MAC addresses	79.2	<i>Nearest device</i> group MAC addresses listed in Table 7-1 of IEEE Std 802.1AB-2009	M	Yes []
TLV2	LLDPDU bit and octet ordering	79.2	Defined in subclause 8.1 of IEEE Std 802.1AB-2009	M	Yes []

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79.5.5 MAC/PHY Configuration/Status TLV

Item	Feature	Subclause	Value/Comment	Status	Support
MPT1	auto-negotiation support/status field	79.3.1.1	Identifies support and current status as defined in Table 79–2	MP:M	Yes [] N/A []
MPT2	PMD auto-negotiation capability field	79.3.1.2	Bitmap of the ifMauAutoNeg-CapAdvertisedBits object defined in IETF RFC 4836	MP:M	Yes [] N/A []
MPT3	operational MAU type field	79.3.1.3	Derived from the list position of the corresponding dot3Mau-Type as listed in IETF RFC 4836 (or subsequent revisions)	MP:M	Yes [] N/A []
MPT4	operational MAU type field	79.3.1.3	Set to zero for MAU types not listed in IETF RFC 4836 (or subsequent revisions)	MP:M	Yes [] N/A []
MPT5	Usage rules	79.3.1.4	LLDPDU contains no more than one MAC/PHY Configuration/Status TLV	MP:O	Yes [] No [] N/A []

79.5.6 EEE TLV

Item	Feature	Subclause	Value/Comment	Status	Support
EET1	Transmit T_w field	79.3.5.1	2 octets representing time (expressed in microseconds) that the transmitting link partner will wait before it starts transmitting data after leaving the LPI mode	EE:M	Yes [] N/A []
EET2	Receive T_w field	79.3.5.2	2 octets representing time (expressed in microseconds) that the receiving link partner is requesting the transmitting link partner to wait before it starts transmitting data following the LPI	EE:M	Yes [] N/A []
EET3	Fallback field	79.3.5.3	2 octets representing time (expressed in microseconds)	EE:O	Yes [] N/A []
EET4	Echo Transmit and Receive T_w fields	79.3.5.4	2 octets representing time (expressed in microseconds)	EE:M	Yes [] N/A []
EET5	Usage rules	79.3.5.5	LLDPDU contains no more than one EEE TLV	EE:O	Yes [] No [] N/A []

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79.5.7 EEE Fast Wake TLV

Item	Feature	Subclause	Value/Comment	Status	Support
EFW1	Transmit fast wake field	79.3.6.1	1 octet representing fast wake option for transmit LPI function	EEFW: M	Yes [] N/A []
EFW2	Receive fast wake field	79.3.6.2	1 octet representing fast wake option for receive LPI function	EEFW: M	Yes [] N/A []
EFW3	Echo Transmit and Receive fast wake fields	79.3.6.3	2 octets representing received fast wake options	EEFW: M	Yes [] N/A []

79.5.8 Power Via MDI TLV

Item	Feature	Subclause	Value/Comment	Status	Support
PVT1	MDI power support field	79.3.2.1	Bit map of the MDI power capabilities and status as defined in Table 79-3	PV:M	Yes [] N/A []
PVT2	PSE power pair field	79.3.2.2	Integer value as defined by the pethPsePortPowerPairs object in IETF RFC 3621	PV:M	Yes [] N/A []
<u>PVT3</u>	<u>PSE power pair field for Type 3 or Type 4 PSEs furnishing power on a single pairset</u>	<u>79.3.2.2</u>	<u>To use value that defines that pairset (signal=Alternative A, spare=Alternative B)</u>	<u>PV:M</u>	<u>Yes [] N/A []</u>
PVT4 3	power class field	79.3.2.3	Integer value as defined by the pethPsePortPowerClassifications object in IETF RFC 3621	PV:M	Yes [] N/A []
PVT5 4	Power type/source/priority field	79.3.2.4	Contains a bit-map of the power type, source, and priority defined in Table 79-4	PV:M	Yes [] N/A []
PVT6 5	Power type field	79.3.2.4.1	Set according to Table 79-4	PV:M	Yes [] N/A []
<u>PVT7</u>	<u>Power type field for Type 3 or Type 4 PSEs</u>	<u>79.3.2.4.1</u>	<u>Set to the value corresponding with Type 2 PSEs</u>	<u>PV:M</u>	<u>Yes [] N/A []</u>
<u>PVT8</u>	<u>Power type field for Type 3 or Type 4 PDs</u>	<u>79.3.2.4.1</u>	<u>Set to the value corresponding with Type 2 PDs</u>	<u>PV:M</u>	<u>Yes [] N/A []</u>
PVT9 6	Power source field when power type is PD	79.3.2.4.2	Set to '01' when powered only through the PI; set to '11' when powered from both; set to '00' when information is not available	PV:M	Yes [] N/A []
PVT10 7	Power source field when power type is PSE	79.3.2.4.2	When sourcing power through the PI, set to '01' when using primary supply; set to '10' when using backup source; set to '00' when information is not available	PV:M	Yes [] N/A []

Item	Feature	Subclause	Value/Comment	Status	Support
PVT11 8	Power priority field when power type is PD	79.3.2.4.3	Set to the power priority configured for the device; set to '00' if power priority is undetermined	PV:M	Yes [] N/A []
PVT12 9	PD requested power value field	79.3.2.5	Contains the PD's requested power value defined in Table 79-5	PV:M	Yes [] N/A []
PVT13 0	PSE allocated power value field	79.3.2.6	Contains the PSE's allocated power value defined in Table 79-6	PV:M	Yes [] N/A []
<u>PVT14</u>	<u>PSE power status value field</u>	<u>79.3.2.6a</u>	<u>Contains the PSE's bit-map of the PSE power pair and PSE power class, defined in Table 79-6a</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT15</u>	<u>PSE power pair value field</u>	<u>79.3.2.6a.1</u>	<u>Integer value for PSE power pairs defined by 33.2.4</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT16</u>	<u>PSE power pair value field for PDs</u>	<u>79.3.2.6a.1</u>	<u>Set to 00</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT17</u>	<u>PSE power class value field</u>	<u>79.3.2.6a.2</u>	<u>Integer for assigned Class by the PSE as defined in 33.2.7</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT18</u>	<u>PSE power class value field for PDs</u>	<u>79.3.2.6a.2</u>	<u>Set to 0000</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT19</u>	<u>System setup value field</u>	<u>79.3.2.6b</u>	<u>Contains the device bit-map of the Power type, PD 4PID, PD PI and PS Load defined in Table 79-6b</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT20</u>	<u>Power type field</u>	<u>79.3.2.6b.1</u>	<u>Set according to Table 79-6b</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT21</u>	<u>PD 4PID field when power type is PD</u>	<u>79.3.2.6b.2</u>	<u>Set according to Table 79-6b</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT22</u>	<u>PD 4PID field when power type is PSE</u>	<u>79.3.2.6b.2</u>	<u>Set to 0</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT23</u>	<u>PD PI field when power type is PD</u>	<u>79.3.2.6b.3</u>	<u>Set according to Table 79-6b</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT24</u>	<u>PD PI field when power type is PSE</u>	<u>79.3.2.6b.3</u>	<u>Set to 0</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT25</u>	<u>PD Load field when power type is PD</u>	<u>79.3.2.6b.4</u>	<u>Set according to Table 79-6b</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT26</u>	<u>Electrically isolated</u>	<u>79.3.2.6b.4</u>	<u>To mean greater than or equal to 50 kohm resistance between any one connection of Mode A and any one connection of Mode B, when measured using at least $V_{Port_PSE-2P_minimum}$ for Type 4 PSEs</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>
<u>PVT27</u>	<u>PD Load field when power type is PSE</u>	<u>79.3.2.6b.4</u>	<u>Set to 0</u>	<u>PV:M</u>	<u>Yes []</u> <u>N/A []</u>

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Item	Feature	Subclause	Value/Comment	Status	Support
PVT28	PD Mode selection field when power type is PD	79.3.2.6b.5	Set according to Table 79-6b to select the Mode for which the PD is requesting power	PV:M	Yes [] N/A []
PVT29	PD Mode selection field when the power type is PSE	79.3.2.6b.5	Set to 0	PV:M	Yes [] N/A []
PVT30	PSE maximum available power field	79.3.2.6c	Contains the highest power the PSE can grant as defined in Table 79-6c while taking the available power budget and hardware capabilities into account	PV:M	Yes [] N/A []
PVT31	Autoclass field	79.3.2.6d	Contains the bits defined in Table 79-6d	PV:M	Yes [] N/A []
PVT32	Request power down field	79.3.2.6e	Set as defined in Table 79-6f	PV:M	Yes [] N/A []
PVT33+	Usage rules	79.3.2.7	LLDPDU contains no more than one Power Via MDI TLV	PV:O	Yes [] No [] N/A []
PVT34	PD measurement values	79.3.7.1	Set to 0	PV:M	Yes [] N/A []
PVT35	PSE measurement values	79.3.7.2	Set to 0	PV:M	Yes [] N/A []
PVT36	PSE power price index field	79.3.7.3	Contains a linear index to the current value of electricity within the PSE while taking the availability of power from any external and internal resources, and the relative supply and demand balance, into account	PV:M	Yes [] N/A []

79.5.9 Link Aggregation TLV

Item	Feature	Subclause	Value/Comment	Status	Support
LAT1	link aggregation status field	79.3.3.1	Bitmap of the link aggregation capabilities and the current aggregation status as defined in Table 79-7	LA:M	Yes [] N/A []
LAT2	aggregated port ID	79.3.3.2	IEEE 802.3 aggregated port identifier, aAggPortID	LA:M	Yes [] N/A []
LAT3	Usage rules	79.3.3.3	LLDPDU contains no more than one Link Aggregation TLV	LA:O	Yes [] No [] N/A []

79.5.10 Maximum Frame Size TLV

Item	Feature	Subclause	Value/Comment	Status	Support
FST1	maximum frame size field	79.3.4.1	Integer value indicating the maximum supported frame size	FS:M	Yes [] N/A []
FST2	maximum frame size field	79.3.4.1	1518 for basic frames	FS:O/1	Yes [] No [] N/A []
FST3	maximum frame size field	79.3.4.1	1522 for Q-tagged frames	FS:O/1	Yes [] No [] N/A []
FST4	maximum frame size field	79.3.4.1	2000 for envelope frames	FS:O/1	Yes [] No [] N/A []
FST5	Usage rules	79.3.4.2	LLDPDU contains no more than one Maximum Frame Size TLV	FS:O	Yes [] No [] N/A []

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