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- 2 This comment is marked "linrush_mess".
 - 1. The baselines approved on Table 33-31 items lirush_PD and lirush_PD-2P was not implemented correctly:
 - (a) March 2016, http://www.ieee802.org/3/bt/public/mar16/darshan_09_0316R6.pdf
 Approved remedy #201 in D1.6 comments file.
 - (b) May 2016, http://www.ieee802.org/3/bt/public/may16/darshan_01_0516_Rev006.pdf
 Approved remedy #29 in D1.7 comments file.
 - 2. Lennart's comment #523 while trying to make it correct in D2.1 by changing the "PD Type" column are creating new problem that will cause Type 3 and Type 4 PDs to be noncompliant.
 - 3. See in Table 33-31 below the diffrences from March 2016 approved baseline to D2.1 including proposal for D2.2.

The new problem is the use of the "assigned class" in Iirush_PD and Iinrush_PD-2P parameters and the new PD Type columns numbers to support it:

- 16 **Example:** Type 4 single-signature class 7-8. As a result, it was designed with linrush circuitry and
- capacitance size to consume linrush for advertised class 7-8. But if it gets power demoted to Class 6,
- according to D2.1 it shall change its linrush circuitry and capacitor size so it can work with Class 6 linrush capability.
 - This is impossible and no PD will switch linrush circuitry per the assigned class.

The facts are:

- 1. PDs of higher power Type/class, when connected to PSEs with lower Type/class are not required to work. They may work but doesn't have to. (Type 2 PD connected to Type 1 PSE doesn't have to work. Same Type_N PD connected to Type_(N+1) PSE.
- 2. Only PDs that was designed to support power demotion will work successfully during linrush and power_on.
- 3. The current spec requires all PDs to switch inrush circuitry as function of the assigned class.
- 4. The current spec required all PDs to support power demotion.
- 5. This is only about PD linrush requirements as function of the assigned class. It is not about PSE linrush as function of the assigned class that remains unchanged.

See suggested remedy below.

Suggested Remedy: See below Table 33-31 column PD Type and column D2.2.

1. Change PD Type column as shown for PD Type column and D2.2 column.

- 2. Change the parameter text for item 6 and 7 as shown below (deleting the assigned class related text).
- 3. Add the following text to 33.3.7.3 (Editor to find the suitable location).

"PDs that support power demotion may need to support linrush_PD and linrush_PD-2P as function of the assigned class."

[Editor to verify that power demotion is defined. If not defined, Editor to suggest one for D2.2]

Item	Parameter	Symbol	Unit	Min	Max	PD T					Additional Information
Input I	nrush current [D2.2] as function of the assign	ed <u>requested</u>	class, v	vhen the	PD is lim	niting th	e curren	t during i	nrush pe	eriod per 3	3.3.7.3.
					Base Lir	ne	D1.7	D1.8,	D2.1	D2.2	
					March/I	May		D2.0			
					2016						
6	Single-signature PD Class 0 to 6.	Inrush- PD	A		0.400	All	All	All	All	All 1,2,3	Peak value see 33.3.7.3
	Single-signature PDs Class 7 to 8.				0.800	4	4	4	4	4	
	Dual-signature PD class 1 to 4				0.500	3	3	3	3,4	3,4 <u>3</u>	
	Dual-signature PD class 5				0.650	4	4	4	4	4	
	Inrush current per pairset [D2.2] as funct per 33.3.7.3.	ion of the a	ssigned	l- <u>requir</u>	ed class,	when t	the PD i	s limitin	g the ci	urrent du	ring inrush
7	Single-signature PD Class 0 to 6.	Inrush- PD-2P	A		0.400	All	3	3	3,4	3,4 <u>3</u>	Peak value see 33.3.7.3
	Single-signature PDs Class 7 to 8.				0.600	4	3,4	3,4	4	4	
	Dual signature PD Class 1 to 4				0.250	3	4	4	3,4	3,4 3	
	Dual signature PD Class 5				0.325	4			4	4	
	Baseline from March and May 2016. A designed according to advertised class	s only as no	ormal P	D woul	d be	ns		ſ		oposed r D2.2	corrections
	designed for (except for class 0-6 row that it meeting(David A.) and "assigned class" (Jean) i thoughts at the meeting).					ch					
	Baseline from March and May 2016 was not implemented correctly.										
	D2.1 changes based on Editorial comm	nent #522 :	and #52	23.							

#	Parameter	Symbol	Units	Min	Max	PSE Type	Additional Information			
	Total output current of both pairsets of the same polarity in the POWER_UP state as function of assigned Class									
6	Single-signature PD Class 0 to 4.	Iinrush	A	0.400	0.450	All				
	Single-signature PD Class 5 to 6.			0.400	0.900	3,4	See 33.2.8.5 and maximum value definition in			
	Single-signature PD Class 7 to 8.			0.800	0.900	4	Figure 33–26. For Type 4 PSEs, see 33.2.8.5.1			
	Dual-signature PD Class 1 to 4.			0.500	0.900	3,4	,			
	Dual-signature PD, Class 5.			0.650	0.900	4				
	Output current per pairset in POWER_UP state as function of the assigned class.									
	Single-signature PD Class 0 to 4.	Iinrush- 2P	A		0.450	3,4	See 33.2.8.5 and			
	Single-signature PD class 5 to 6.				0.600	3,4	maximum value definition in Figure 33–			
	Single-signature PD Class 7 to 8.				0.600	4	26. For Type 4 PSEs, see 33.2.8.5.1			
	Dual-signature PD Class 1 to 4.			0.250	0.600	3,4				
7	Dual-signature PD, Class 5.			0.325	0.600	4				