

COMMENT r01- 404, PAGE 88 LINE 32
COMMENT r01- 2, 404 PAGE 210 LINE 7
COMMENT r01- 463, PAGE 210 LINE 7

Suggested Remedy

1. Make the following changes:

"This field shall be set according to Table 79-6d when the power type is PD.
~~Electrically isolated for this bit field shall mean greater than or equal to 50 k ohm resistance between any one connection of Mode A and any one connection on Mode B, when measured using at least VPort_PSE-2P minimum for Type 4 PSEs.~~ This field shall be set to 0 when the power type is PSE. [See details 145.4.1.](#)"

2. Add the following text to 145.4.1 page 209 after line 38:

Dual-signature PDs shall have less than or equal to 10uA of current between any one conductor of Mode A and any one conductor of Mode B when VPD, as defined in 145.1.3, of either Mode is less than Voff_PD min, as defined in Table 145-29. See Table 79-6d.