

Comment i-455 (145B.1.4 P 268, L 268)

CC_DET_SEQ=3 means: Connection check is followed by staggered detection.

Figure 145B-11 for dual-signature PD shows that CC_DEC_SEQ=3 is only possible when the Detection of the 2nd pairset starts after Tpon +Tx of 1st pairset which is possible but not the only one possible for staggered detection. This create confusion due to lake of clear definitions.

It is possible that specifically for CC_DET_SEQ=3, the intent was to do detection on secondary when primary is on. This is fine but not defined anywhere that this is the intent.

We need:

First, to define what is "parallel" detection and what is "staggered" detection in the definition of the constant CC_DET_SEQ.

Next, to verify that the state machine is in sync to those definitions with all CC_DET_SEQ OPTIONS (0,1,2,3).

Last, to verify if the drawings in Annex 145B are covering the CC_DET_SEQ OPTIONS (0,1,2,3) for single and dual-signature.

Starting with definitions in page 109 line 36:

Proposed Remedy:

145.2.5.3 Constants

CC_DET_SEQ

A constant indicating the sequence in which the PSE performs connection check and detection. See Annex 145B for timing diagrams.

Values:

0: Connection Check is followed by staggered detection for a single-signature PD and parallel detection for a dual-signature PD.

1: Detection on a pairset is followed by connection check and then detection on the other pairset for a single-signature PD and parallel or staggered (starting with first pairset) detection for a dual-signature PD. *[Yair: The text is not clear. Modifying to check if the intent was]:*

1: Detection on a pairset is followed by connection check and then detection on the other pairset for a single-signature PD. For dual-signature PD, detection on a pairset is followed by connection check and then parallel or staggered detection on the other pairset.

2: Connection check and detection on both pairsets are performed within a single Tdet window.

3: Connection check is followed by staggered detection.

Option 1 for definitions:

Add the following:

See Table XXX for parallel detection and staggered detection definitions:

	Single-signature PD	Dual-signature PD
Parallel detection	Detection on both pairsets is performed within a single Tdet window	Detection on both pairsets happens before any pairset is powered
Staggered detection	NA	Detection on the second pairset happens after the first pairset is powered



Option 2 for definitions:

Not part of the baseline

The following definition is more flexible. It allows using class_4PID_mult_events_pri=TRUE and yet turning ON both pairsets together which impossible with the definition in option 1 for dual-signature when using CC_DET_SEQ=3 and class_4PID_mult_events_pri=TRUE (although it is possible by other CC_DET_SEQs).

Add the following:

See Table XXX for parallel detection and staggered detection definitions:

	Single-signature PD	Dual-signature PD
Parallel detection	Detection on both pairsets is performed within a single Tdet window	Detection on both pairsets happens before any pairset is powered
Staggered detection	NA	Detection on the second pairset happens before or after the first pairset is powered

If the above definitions are acceptable:

Add the following drawing (Figure 145B-11A) after Figure 145B-11.

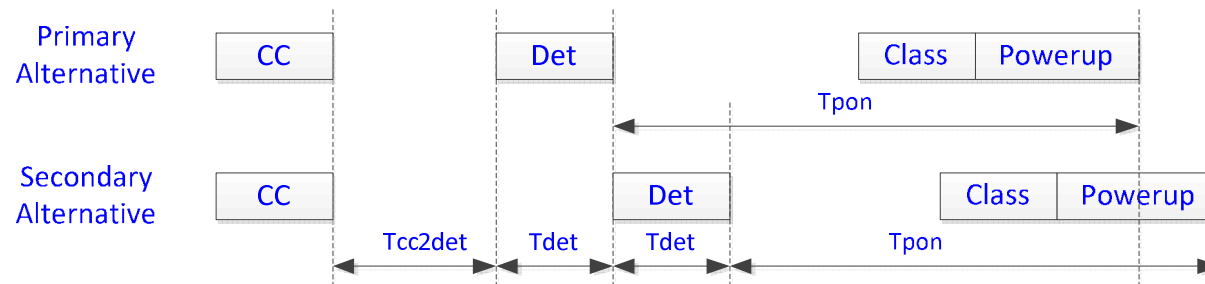


Figure 145B–11A—PSE implementing CC_DET_SEQ=3, do_cxn_chk result is dual, staggered detection

