

Comment i-433, i-110, i-393, i-429 (145.2.8.5.3, P162, L10)

Figure 145-22 need updates due to few issues:

1. Clear boundaries for the PSE assuming we agree that for unbalance purposes the equipment connector is in the PI.
2. The link section arrow and label is not required since this is a test fixture and all the required information are embedder in the component names.
3. The shape of the load need to represent constant power sink. All our spec is since the PD load is constant power sink.
4. Consider changing Figure 145-22 title to "PSE PI unbalance test verification model for Table 145-17.
5. Keep the names of the components as is since it adds sense and clarity to understand the P2PUnb issue without the need to go to the annexes. The annexes meant to more detailed explanations.

p.s. if we will not use the constant power sink (in addition to the fact that spec numbers will not be met..) e.g. implementers will use constant current load as some folks though to use, Lennart will not get his margins for Icon-2P_unb for the cases that $V_{pse} > V_{pse_min}$. ☹️

Proposed Remedy:

