

### ***Justification for Removing TEST\_MODE***

*TEST\_MODE is out of scope of this power standard and is best left for proprietary implementation.*

- Forcing power to non-compliant PDs is out of scope of this standard
- Leaving it in encourages continuing complication of this standard
- TEST\_MODE implies a hole in interoperability, given its lack of specification and may create interoperability problems in the field

#### **30.9.1.1.4 aPSEPowerDetectionStatus**

*Page 36 Line 10*

See darshan\_05\_0917 for changes to remove TEST\_MODE references.

#### **145.2.5.1**

Page 108 Line 36

In the state diagram, each Alternative serves a distinct role during 4-pair operation. In any implementation, the roles of the Alternatives shall be established in IDLE ~~or TEST\_MODE~~ and be maintained in every other state. In the state diagram, the roles of the Alternatives are named Primary Alternative and Secondary Alternative.

#### 145.2.5.4

Page 110 Line 33

alt\_pwrd\_pri

A variable that controls the circuitry that the PSE uses to power the PD over the Alternative that has been assigned as Primary.

Values:

FALSE: The PSE is not to apply power to the Primary Alternative.

TRUE: The PSE has detected, classified, and will power a PD on the Primary Alternative; ~~or power is being forced on the Primary Alternative in TEST\_MODE.~~

Page 110 Line 40

alt\_pwrd\_sec

A variable that controls the circuitry that the PSE uses to power the PD over the Alternative that has been assigned as Secondary.

Values:

FALSE: The PSE is not to apply power to the Secondary Alternative.

TRUE: The PSE has detected, classified, and will power a PD on the Secondary Alternative; ~~or power is being forced on the Primary Alternative in TEST\_MODE.~~

Page 111 Line 52

Remove force\_pwr\_pri and force\_pwr\_sec

~~force\_pwr\_pri~~

~~This variable indicates if the Primary Alternative is to apply power to the pairset while in TEST\_MODE (see Table 145-3).~~

~~Values:~~

~~FALSE: The Primary Alternative is not powered.~~

~~TRUE: The Primary Alternative is powered.~~

~~force\_pwr\_sec~~

~~This variable indicates if the Secondary Alternative is to apply power to the pairset while in TEST\_MODE (see Table 145-3).~~

~~Values:~~

~~FALSE: The Secondary Alternative is not powered.~~

~~TRUE: The Secondary Alternative is powered.~~

Page 116 Line 42

pse\_enable

A control variable that selects PSE operation ~~and test functions.~~

Values:

disable: All PSE functions disabled (behavior is as if there was no PSE functionality).

enable: Normal PSE operation.

~~force\_power: Test mode selected that causes the PSE to apply power to the PI when there are no detected error conditions.~~

### 145.2.5.7

Remove entire TEST\_MODE page

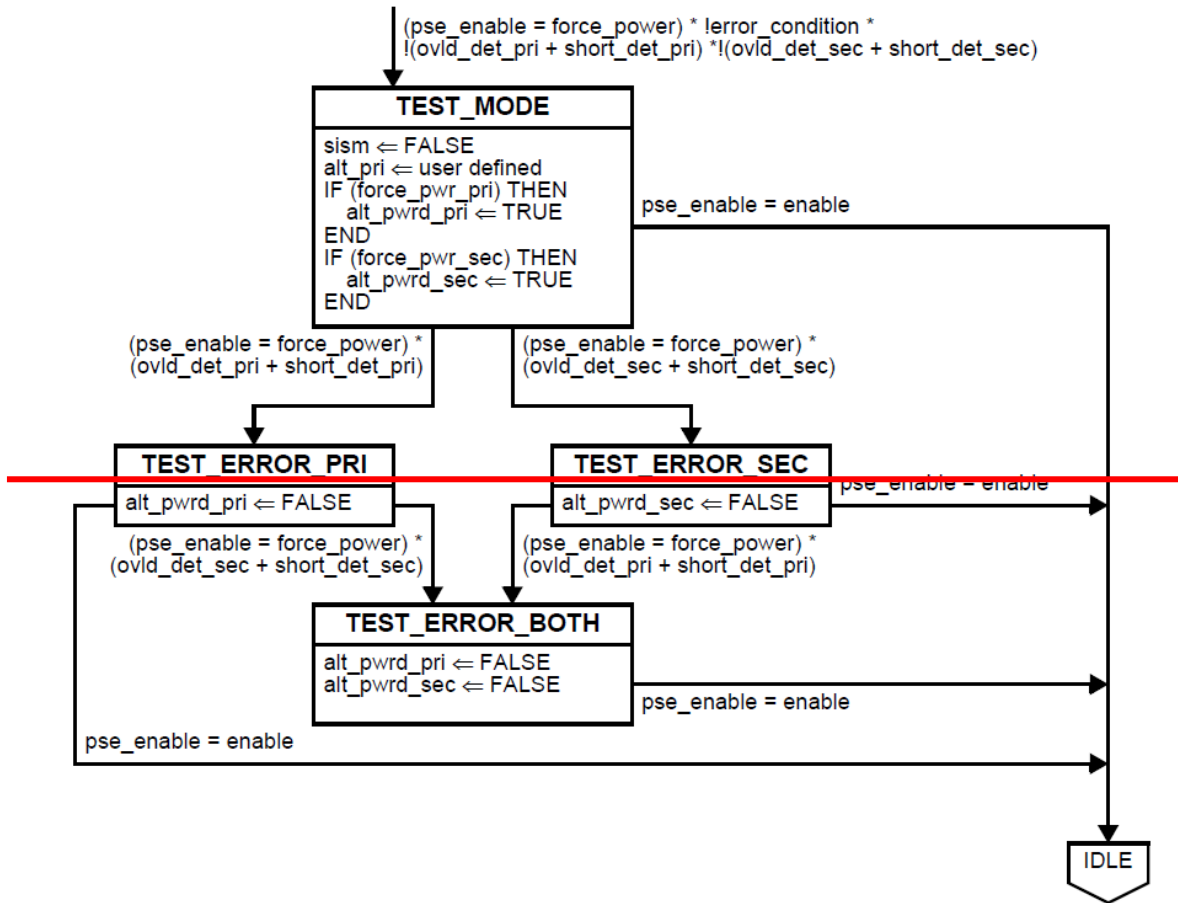


Figure 145–13—Top level PSE state diagram (continued)

Editor to be granted editorial license to remove references to test modes either in the current draft or draft 3.0 comment resolutions.