

25 Gb/s Ethernet Task Force Architecture Ad Hoc Report

Matt Brown, APM
Architecture Ad Hoc Chair

Report

- 4 ad hoc calls since Nov. interim
 - Nov. 19, Dec. 3, Dec. 17, Jan. 7
- 96+ attendees
- 13 contributions presented (see next slide)
- Meeting minutes and presentation materials:
<http://www.ieee802.org/3/by/public/adhoc/architecture/index.html>
- Further work: TBD
- Next meeting: TBD

Presentations

- **Project**
 - “25GE Study Group Update”, Mark Nowell
- **Copper Cables**
 - “A Simplifying 25Gb/s Cable Proposal”, Rich Mellitz
 - “25G Cable Assembly Models”, Chris Diminico
 - “25GE host/cable options”, Mark Nowell
 - “Cable Assembly Specification”, Chris DiMinico
 - “Thoughts on 25G cable/host configurations”, Mike Dudek
 - “Thoughts on cable and host options”, Rich Mellitz
- **PCS/FEC**
 - “PCS and FEC baseline proposal”, Eric Baden
- **Auto-Negotiation**
 - “Auto-Negotiation for 25G-CR Cables”, Venugopal Balasubramonian
 - “Baseline Proposal for 802.3by Auto-Negotiation”, Arthur Marris
- **Backplane**
 - “A Baseline Proposal for 25GBASE-KR PMD” – Adee Ran
- **XXVAUI**
 - “Proposed Baseline text for: Chip-to-module 25 Gb/s one-lane Attachment Unit Interface (XXVAUI-1)” – Tom Palkert
 - “Proposed Baseline text for: Chip-to-chip 25 Gb/s one-lane Attachment Unit Interface (XXVAUI-1)” – Tom Palkert
- **PMA**
 - “25GE PMA sublayer baseline proposal” – Matt Brown