25 Gb/s 100 m MMF reach objective baseline proposal

Reviewed and edited by MMF ad hoc

Presented by Jonathan King, Finisar

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Outline

- Baseline proposal of a retimed 25Gb/s PMD to address the *P802.3by* objective to 'Provide physical layer specifications which support link distances of at least 100 m over MMF'
  - Single lane, short wavelength based PMD for 25GBASE-SR.
  - Leveraging 100GBASE-SR4 optics, and CAUI-4 25 Gb/s electrical interface.
  - Assumed use of 100GBASE-SR4 FEC, or similar strength FEC (to be defined in 802.3by), to enable 100 m reach.
  - Architecture, parameters and specifications for optical interfaces follow.

Supporters and contributors

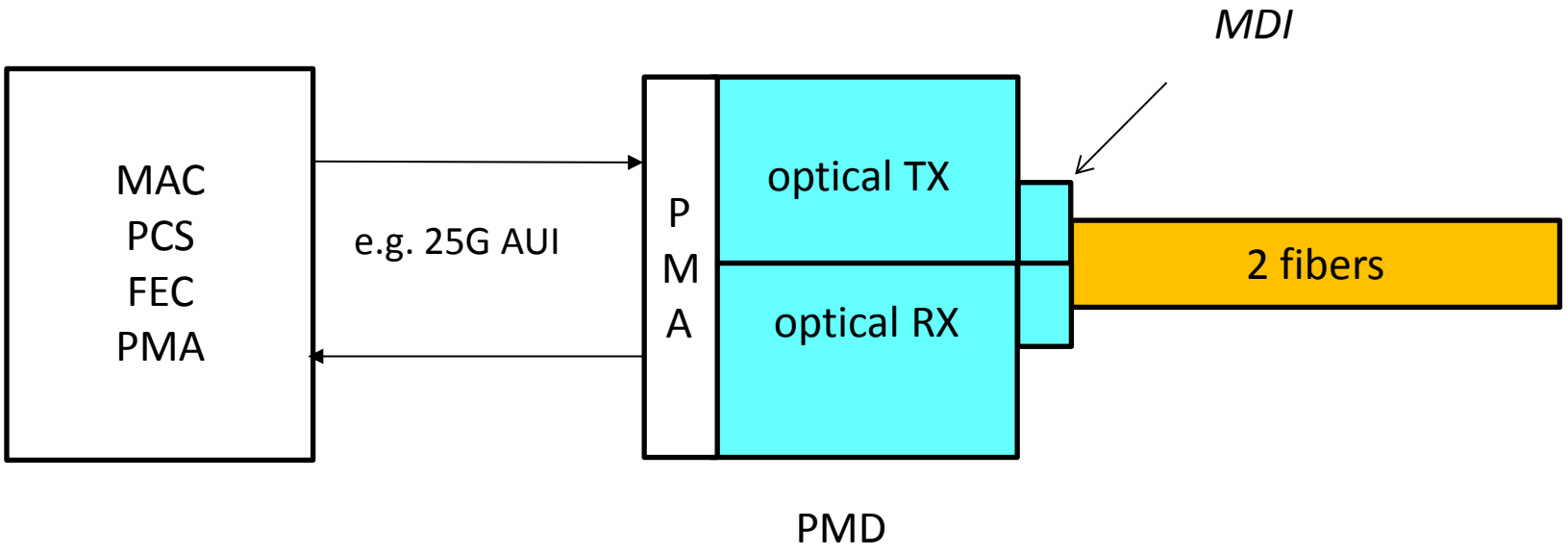
- John Abbott, Corning
- Doug Coleman, Corning
- Piers Dawe, Mellanox
- Dan Dove, DNS/Emulex
- Mark Gustlin, Xilinx
- Jonathan King, Finisar
- Paul Kolesar, Commscope
- Brett Lane, Panduit
- Greg LeCheminant, Keysight Technologies
- John Petrilla, Avago Technologies
- Rick Pimpinella, Panduit
- Kapil Shrikhande, Dell
- Steve Swanson, Corning
- Nathan Tracy, TE

Motivation

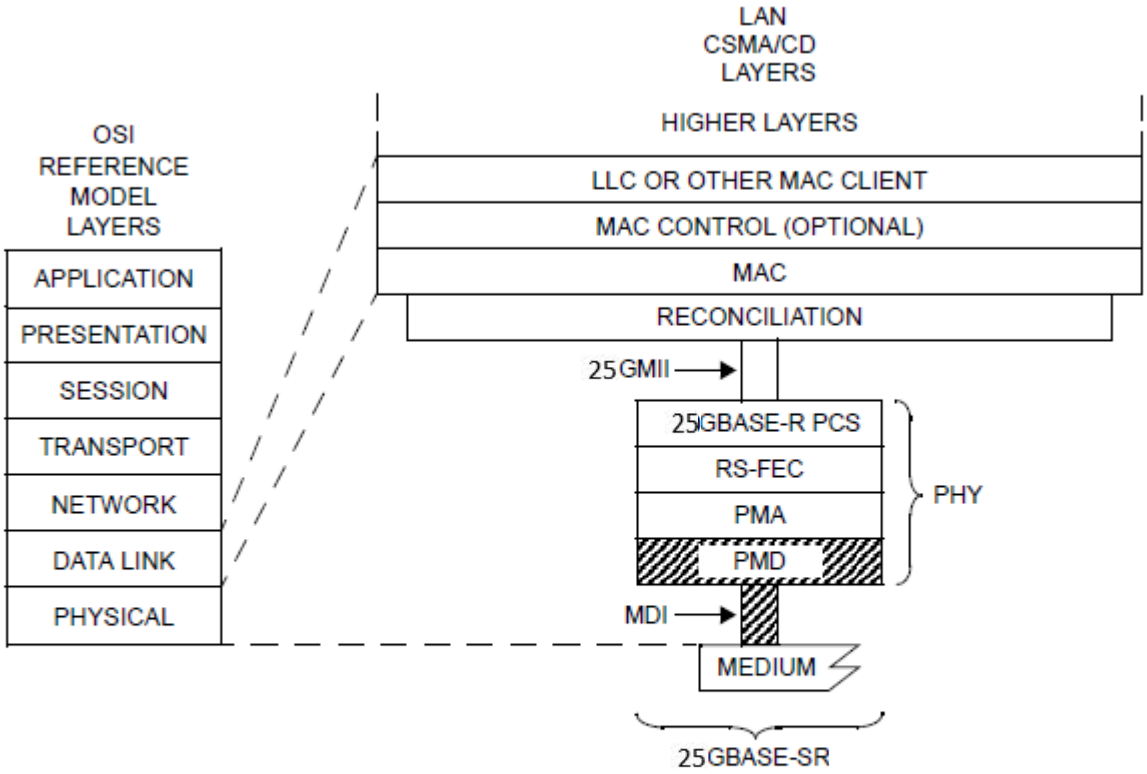
- Single lane link operating at 25.78125 GBd utilizes low cost, high performing multimode fiber compatible optics and electronics
  - Leverages 100GBASE-SR4 technology
  - FEC-supported retimed interface enables a lowest power, lowest cost, 100 m solution today
  - Uses existing, viable semiconductor technologies and uncooled VCSELs
- The single optical lane directly maps to the single electrical lane of 25G AUI, without requiring multiplexing, translation, or de-skewing inside the module.
- This proposal is supported by multiple vendors and users, and is economically feasible and competitive compared to other alternatives.

Proposal

- Single lane @ 25.78125 GBd for 25GBASE-SR over 100 m OM4 fiber.
  - Exact signaling rate is determined by project's choice of FEC.
- 850 nm sources, re-use of single lane of 100GBASE-SR4 specifications.
  - Assumes target BER (prior to error correction) around $5x10^{-5}$, similar to 100GBASE-SR4.



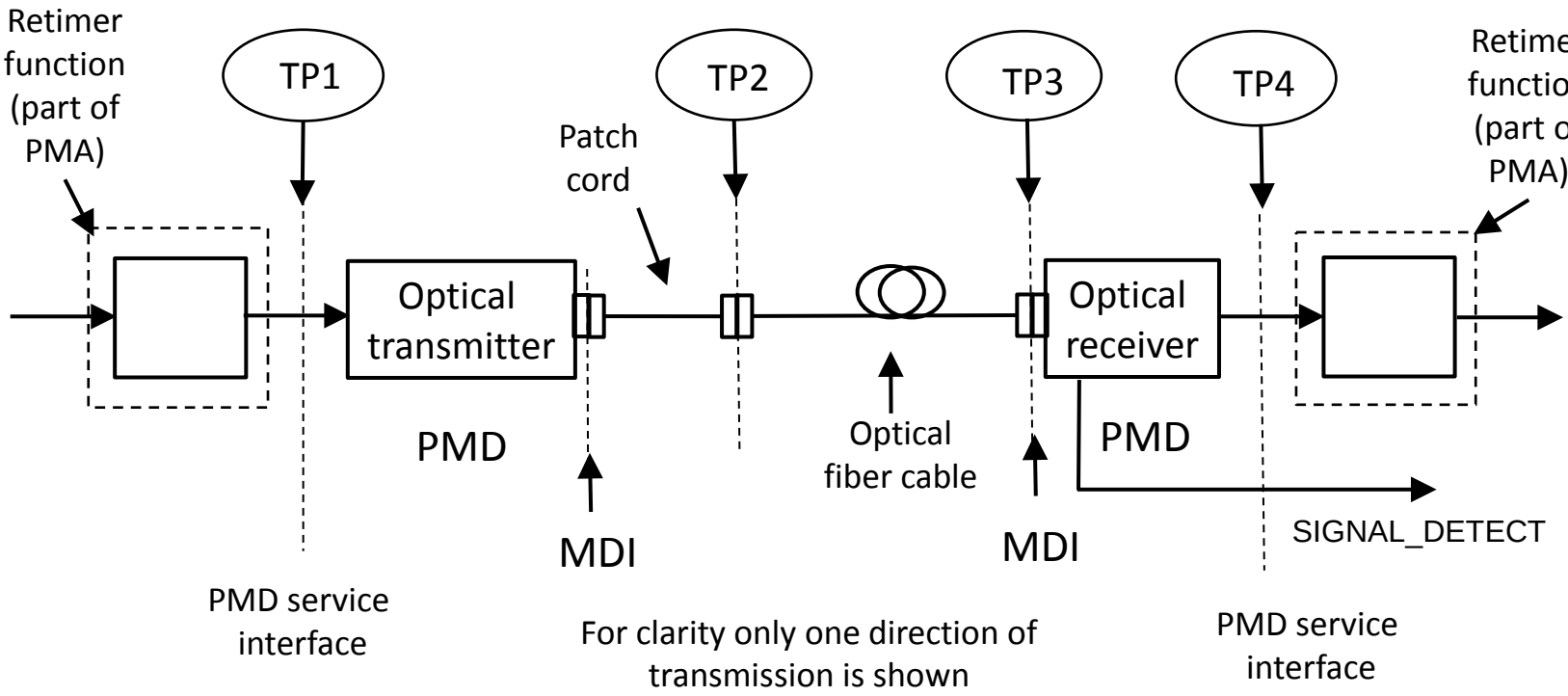
Position in 802.3 architecture



25GMII = 25Gb/s MEDIA INDEPENDENT INTERFACE
LLC = LOGICAL LINK CONTROL
MAC = MEDIA ACCESS CONTROL
MDI = MEDIUM DEPENDENT INTERFACE
PCS = PHYSICAL CODING SUBLAYER
PHY = PHYSICAL LAYER DEVICE
PMA = PHYSICAL MEDIUM ATTACHMENT
PMD = PHYSICAL MEDIUM DEPENDENT
RS-FEC = REED-SOLOMON FORWARD ERROR CORRECTION
SR = PMD FOR MULTIMODE FIBER

Editor's note: The RS-FEC layer may be merged into 25GBASE-R PCS layer, depending on the choice of architecture by the Task Force.

Block diagram for 25GBASE-SR transmit/receive path



Editors note: TP1 and TP4 are not expected to be accessible

PMD Optical specifications

- Transmitter characteristics at TP2 follow a single lane of 100GBASE-SR4, Clause 95, Table 95-6.
- Receiver characteristics at TP3 follow a single lane of 100GBASE-SR4, Clause 95, Table 95-7.
- Illustrative link power budget follows 100GBASE-SR4, Clause 95, Table 95-8.
  - Current status of these tables shown on next 3 slides

Transmitter characteristics (each lane) at TP2: follow 100GBASE-SR4, Clause 95, Table 95-6 (D3.3 illustrated below)

Description	Value	Unit
Signaling rate, each lane (range)	25.78125 ± 100 ppm	GBd
Center wavelength (range)	840 to 860	nm
RMS spectral width[a] (max)	0.6	nm
Average launch power, each lane (max)	2.4	dBm
Average launch power, each lane (min)	–8.4	dBm
Optical Modulation Amplitude (OMA), each lane (max)	3	dBm
Optical Modulation Amplitude (OMA), each lane (min)[b]	–6.4	dBm
Launch power in OMA minus TDEC (min)	–7.3	dBm
Transmitter and dispersion eye closure (TDEC), each lane (max)	4.3	dB
Average launch power of OFF transmitter, each lane (max)	–30	dBm
Extinction ratio (min)	2	dB
Optical return loss tolerance (max)	12	dB
Encircled flux[c]	≥ 86% at 19 μm ≤ 30% at 4.5 μm	
Transmitter eye mask definition {X1, X2, X3, Y1, Y2, Y3} Hit ratio 1.5×10^{-3} hits per sample	{0.3, 0.38, 0.45, 0.35, 0.41, 0.5}	

[a]RMS spectral width is the standard deviation of the spectrum.
[b]Even if the TDEC < 0.9 dB, the OMA (min) must exceed this value.
[c]If measured into type A1a.2 or type A1a.3 50 μm fiber in accordance with IEC 61280-1-4.

Receiver characteristics (each lane) at TP3: follow 100GBASE-SR4, Clause 95, Table 95-7 (D3.3 illustrated below)

Description	Value	Unit
Signaling rate, each lane (range)	25.78125 ± 100 ppm	GBd
Center wavelength (range)	840 to 860	nm
Damage threshold[a] (min)	3.4	dBm
Average receive power, each lane (max)	2.4	dBm
Average receive power, each lane[b] (min)	–10.3	dBm
Receive power, each lane (OMA) (max)	3	dBm
Receiver reflectance (max)	–12	dB
Stressed receiver sensitivity (OMA), each lane[c] (max)	–5.2	dBm
Conditions of stressed receiver sensitivity test:[d]		
Stressed eye closure (SEC), lane under test	4.3	dB
Stressed eye J2 Jitter, lane under test	0.39	UI
Stressed eye J4 Jitter, lane under test (max)	0.53	UI
OMA of each aggressor lane	3	dBm
Stressed receiver eye mask definition {X1, X2, X3, Y1, Y2, Y3} Hit ratio 5×10^{-5} hits per sample	{0.28, 0.5, 0.5, 0.33, 0.33, 0.4}	

[a]The receiver shall be able to tolerate, without damage, continuous exposure to an optical input signal having this average power level on one lane. The receiver does not have to operate correctly at this input power.
[b]Average receive power, each lane (min) is informative and not the principal indicator of signal strength. A received power below this value cannot be compliant; however, a value above this does not ensure compliance.
[c]Measured with conformance test signal at TP3 (see 95.8.8) for the BER specified in 95.1.1.
[d]These test conditions are for measuring stressed receiver sensitivity. They are not characteristics of the receiver.

Illustrative link power budget: follow 100GBASE-SR4, Clause 95, Table 95-8 (D3.3 illustrated below)

Parameter	OM3	OM4	Unit
Effective modal bandwidth at 850 nm[a]	2000	4700	MHz.km
Power budget (for max TDEC)	8.2		dB
Operating distance	0.5 to 70	0.5 to 100	m
Channel insertion loss[b]	1.8	1.9	dB
Allocation for penalties[c] (for max TDEC)	6.3		dB
Additional insertion loss allowed	0.1	0	dB

[a]per IEC 60793-2-10.
[b]The channel insertion loss is calculated using the maximum distance specified in Table 95–5 and cabled optical fiber attenuation of 3.5 dB/km at 850 nm plus an allocation for connection and splice loss given in 95.11.2.1.
[c]Link penalties are used for link budget calculations. They are not requirements and are not meant to be tested.

Further work

- The PMD target BER is likely to deviate from $5x10^{-5}$, so some fine tuning of parameters may be required.
  - The project's choice of FEC, and the division of error budget between 25GBASE-SR and 25G AUI, will determine the pre-FEC BER target, and may also affect the exact signaling rate