

# Agenda for the Dec 17 meeting

25 Gb/s Ethernet Study Group (25GSG)

Architecture Ad Hoc

# 25GSG ad hoc communications

- The 25GSG ad hoc web page is here:
  - <http://www.ieee802.org/3/25GSG/public/adhoc/index.html>
  - Includes ad hoc meeting schedule and WebEx information.
- All study group e-mail communications are via the 25GSG reflector
  - archive and subscription links are here:  
<http://www.ieee802.org/3/25GSG/index.html>

# Attendance

- Attendee names and affiliations for these ad hoc meetings will be taken from the Webex participants list.
- Please ensure that your full name and employer/affiliation are indicated correctly.
- If the Webex participants list does not correctly indicate your name and employer/affiliation, please send an e-mail to the ad hoc recording secretary, Kent Lusted (kent dot c dot lusted at intel dot com).

# Proposed agenda

- Approval of the Agenda
- IEEE patent policy reminder
  - <http://www.ieee802.org/3/patent.html>
- Approval of draft minutes for Dec. 3
- 25GE Study Group Update (verbal) – Mark Nowell
- 25GE Optical Ad Hoc Update (verbal) – Jonathan King
- “PCS and FEC baseline proposal” – Eric Baden (~8:05AM PT)
- “Auto-Negotiation for 25G-CR Cables” – Venugopal Balasubramonian (~8:30AM PT)
- “A Simplifying 25Gb/s Cable Proposal” – Rich Mellitz (~8:50AM PT)
- “25G Cable Assembly Models ” – Chris Diminico (~9:10AM PT)
- Next ad hoc meeting is scheduled for Wednesday Jan. 7
  - Requests due by end of day Monday Jan. 5