

Comment #108 - Overloaded 25GBASE-CR term

Kent Lusted

- Adopted 2 PHY types for cable and backplane at March 2015 plenary meeting:
 - 25GBASE-CR (and 25GBASE-KR)
 - 25GBASE-CR-S (and 25GBASE-KR-S)
- Implemented in P802.3by Draft 1.0

- The term 25GBASE-CR is used to refer to a PMD, a PHY type, a cable assembly label, a host, an MDI, and a link... 25GBASE-CR-S makes sense with some of these, but not with others.

- There are places in the draft that refer to both PHYs types explicitly
 - Clause 110.3: “25GBASE-CR and 25GBASE-CR-S PHYs may be extended using a 25G-AUI chip-to-chip...”
- There are places where 25GBASE-CR means both PHY types
 - Clause 110.10: “25GBASE-CR cable assemblies are intended as a point-to-point interface between 25GBASE-CR or 25GBASE-CR-S PHYs, using controlled impedance cables.”

- Is there interest in exploring potential improvements?