Minutes P802.3by 25 Gb/s Ethernet Task Force ad-hoc meeting – April 15, 2015

Prepared by Kent Lusted

Proposed Agenda:

- Approval of the Agenda
- IEEE patent policy reminder (see http://www.ieee802.org/3/patent.html)
- Approval of draft minutes for March 4 2015
- P802.3by Task Force Update (tentative), Mark Nowell
- P802.3by D1.0 editorial update (tentative), Matt Brown
- "CWMs in 25G Ethernet" – Eric Baden

Presentations posted at: http://www.ieee802.org/3/by/public/adhoc/architecture/index.html

Meeting began at 8:02 a.m. Pacific.

Meeting began with the agenda presentation:
http://www.ieee802.org/3/by/public/adhoc/architecture/agenda_041515_25GE_adhoc-v2.pdf

Matt Brown showed the links to the ad hoc page and the email reflector.

Matt Brown reviewed the Attendance information related to the ad hoc. He reminded participants to indicate full names and employer/affiliation correctly for the meeting minutes.

Matt presented the proposed agenda. There was a proposal to change the editor update reference from D0.1 to D1.0. No one responded. The agenda was approved by the ad hoc.

Matt reminded participants of the IEEE patent policy. (see http://www.ieee802.org/3/patent.html)   No one responded. Matt noted that the policy has changed as was announced in last week's ad hoc meeting. The summary from the original announcement is provided here:

Updates to the IEEE-SA Patent Policy (Clause 6 of the IEEE-SA Standards Board Bylaws) became effective on 15th March 2015. Available from http://standards.ieee.org/about/sasb/patcom/materials.html , the following have been updated:

a) *Patent Slides for Standards Development Meetings*
b) *Patent Letter of Assurance (LOA) Form*
c) *Patent Letter of Assurance (LOA) Cover Letter*
d) *FAQs: Understanding Patent Issues during IEEE Standards Development*
e) *Relationship of IEEE-SA Patent Policy/LoAs to Modification of IEEE Standards by Other Standards Bodies*

The April 8 meeting minutes were posted shortly after the meeting. No comments were received. Matt Brown asked if there were objections to approving the minutes as posted. No one responded. The April 8 meeting minutes were approved by the ad hoc.

25GE Task Force update – Mark Nowell
* Draft 1.0 was posted on the private site. Ballot closes on May 1.
* Task Force will meet in May at the interim meeting. This Task Force will meet at the end of the week. Meeting will start on Wednesday afternoon and run through Friday afternoon. Plan travel accordingly.
* Presentation requests are due May 8. Presentations are due on May 12. Mark will prioritize contributions focusing on comments against D1.0 and BTI.

P802.by D0.1 editorial update – Matt Brown
See http://www.ieee802.org/3/by/public/adhoc/architecture/agenda_041515_25GE_adhoc.pdf
* Draft 1.0 Ballot is currently open. Closes on May 1, end of day, AOE.
* Please send comments prior to the closure, if possible. This enables the editorial team to review and prepare responses in a timely manner.
* Reviewed items requiring work and consensus building.

Presentation #1:
"CWMs in 25G Ethernet", Eric Baden
See http://www.ieee802.org/3/by/public/adhoc/architecture/baden_041515_25GE_adhoc.pdf
* Reviewed the motion adopting the PCS and FEC baseline, the proposals presented at the March plenary meeting, and the results of motion #3.
* Discussed the potential for a latency difference in the overall implementation between the two different mechanisms discussed.
* There was a comment that the 200us lock time on slide 7 is closer to 400us. The lock time for SnT is ~2.5 times longer than the CWM method. Eric will correct the slide and repost.
* Reviewed RCWM approach for EEE wake lock method. The EEE wake lock method is a big ticket item to address in Draft 1.0.
* Discussed the impact of CWMs to OTN. Pete Anslow noted the OTN reference presentation trowbridge_3by_01_0115.pdf from the January task force meeting.
* There were some concerns with the interaction of IEEE 1588. The CWM insertion/deletion and related idle deletion/insertion could affect 1588 time-stamping in end to end link.
* Discussed the potential impact to FlexE. There was agreement to discuss it offline.
* Reviewed motivation for retaining CWMs in 25G RS-FEC mode was to facilitate a 2 lane 50 Gb Ethernet PHY. The validity of a 2 lane 50 Gb solution was questioned.
* Mark Nowell noted that Draft 1.0 implements the CWM as described in the presentation. This PCS implementation choice has been discussed since September 2014.
* With respect to the conclusion on slide 7, it was noted that technology in other standards is a valid consideration.
* Much discussion on the implementation similarities and differences of IEEE 10G and Fibre Channel.

Matt asked if there were objections to extending the meeting for 10 minutes. Objections were raised.

Matt reminded late attendees to mute phones when joining the call.

Next ad hoc meeting will be April 22.

The ad hoc meeting ended at 9:33 a.m. Pacific.

List of attendees (captured from Webex tool)

Adee Ran	intel
Adrian Butter	us.ibm
Ali	independent
Andre Szczepanek	inphi
anil	brocade
Brad Booth	microsoft
Carlo Tosetti (Cisco)	cisco
Chris DiMinico	ieee
Chris Roth (Molex)	molex
Dale Murray	lightcounting
Dan Cunningham (Arista)	arista
Dan Dove	dovenetworking
David Malicoat	hp
David Ofelt	juniper
Duane Remein	huawei
Erdem Matoglu	amphenol-tcs
Eric Baden (Broadcom)	broadcom
Gary Nicholl	cisco
george zimmerman	Commscope & Aquantia
Gianpiero	cisco
hfrazier	broadcom
Ingvar Froroth	marvell
Jeff H	ranovus
Jeff Slavick	avagotech
Jing Fang	marvell
John Nelson(Arista)	arista
jonathan king	finisar
Juan-Carlos Calderon	inphi

Ken Van Orman	spirent
Kenneth Jackson	sei-device
Kent Lusted (Intel)	intel
Madhavi	barefootnetworks
Mark Gravel	hp
Mark Nowell	cisco
martin	altera
martin white	caviumnetworks
Matt Brown	apm
Megha Shanbhag	te
Mike Andrewartha	microsoft
Mike Dudek	qlogic
Nathan Tracy	te
Oded Wertheim	mellanox
Omer Sella	mellanox
Paul Kolesar	commscope
Peter Anslow	ciena
Philip Sun	marvell
Piers	mellanox
Pirooz Tooyserkani	cisco
rakesh sambaraju	nexans
Randy k Rannow	independent
Raymond Nering	cisco
Remy Chang	barefootnetworks
Rich Mellitz (Intel)	intel
Rick Rabinovich	alcatel-lucent
Rita Horner	synopsys
rob stone	broadcom
Scott Irwin	mosys
Scott Kipp	brocade
Scott Sommers	molex
Susan Bueti	ibm
Thananya	ixia
Thomas Brown	vitesse
Tom Palkert	visi
Venu B (Marvell)	marvell
Vittal Balasubramanian	dell
Wei Jiang	ibm
Yasuo Hidaka	fujitsu

Yong Kim (Broadcom)	broadcom
Zvi Rechtman	mellanox