

## Minutes P802.3by 25 Gb/s Ethernet Task Force ad-hoc meeting – May 6, 2015

Prepared by Kent Lusted

### Proposed Agenda:

- Approval of the Agenda
- IEEE patent policy reminder (see <http://www.ieee802.org/3/patent.html>)
- Approval of draft minutes for April 29 2015
- P802.3by Task Force Update (tentative), Mark Nowell
- P802.3by D1.0 editorial update (tentative), Matt Brown
- “Two ways of showing 25G-AUI compliance”, Piers Dawe
- “Use of RCWMs in Clause 108 RSFEC EEE”, Don Cober
- “Comment #108 - Overloaded 25GBASE-CR term”, Kent Lusted
- “P802.3by Draft 1.0 comment summary”, Matt Brown

Presentations posted at: <http://www.ieee802.org/3/by/public/adhoc/architecture/index.html>

Meeting began at 8:03 a.m. Pacific.

Meeting began with the agenda presentation:

[http://www.ieee802.org/3/by/public/adhoc/architecture/agenda\\_050615\\_25GE\\_adhoc.pdf](http://www.ieee802.org/3/by/public/adhoc/architecture/agenda_050615_25GE_adhoc.pdf)

Matt Brown showed the links to the ad hoc page and the email reflector.

Matt Brown reviewed the Attendance information related to the ad hoc. He reminded participants to indicate full names and employer/affiliation correctly for the meeting minutes. Matt reminded participants to mute their lines when not speaking.

Matt presented the proposed agenda. Matt noted that this meeting will be 2 hours in duration to address the proposed topics. No one responded. The agenda was approved by the ad hoc.

Matt reminded participants of the IEEE patent policy. (see <http://www.ieee802.org/3/patent.html>) No one responded.

The April 29 meeting minutes were posted shortly after the meeting. No comments were received. Matt Brown asked if there were objections to approving the minutes as posted. No one responded. The April 29 meeting minutes were approved by the ad hoc.

### **25GE Task Force update – Mark Nowell**

- Task Force will meet in May at the interim meeting in Pittsburgh, PA, USA. This Task Force will meet at the end of the week. Meeting will start on Wednesday afternoon and run through Friday afternoon. Plan travel accordingly.

- Presentation requests are due May 8. Presentations are due on May 12. Mark will prioritize contributions focusing on comments against D1.0 and BTI.

### **P802.by D0.1 editorial update – Matt Brown**

See [http://www.ieee802.org/3/by/public/adhoc/architecture/agenda\\_041515\\_25GE\\_adhoc.pdf](http://www.ieee802.org/3/by/public/adhoc/architecture/agenda_041515_25GE_adhoc.pdf)

- Comments received against Draft 1.0 were posted on May 4. Total of 196 comments.
- Expect to post proposed responses by May 14.
- Reviewed items requiring work and consensus building.
- Reviewed hot topics from Draft 1.0 comment summary.

Matt reminded participants to mute their line when not speaking.

### **Presentation #1:**

“Two ways of showing 25G-AUI compliance”, Piers Dawe

See [http://www.ieee802.org/3/by/public/adhoc/architecture/dawe\\_050615\\_25GE\\_adhoc.pdf](http://www.ieee802.org/3/by/public/adhoc/architecture/dawe_050615_25GE_adhoc.pdf)

- Reviewed major options and motivation.
- Reviewed proposed procedures and specs.
- Discussed assumption of extrapolating to 1e-8 instead of 1e-15
- Discussed relevance of 100GBASE-LR4 support on slide 7. There is no proposal to add a single mode fiber solution to this project.

### **Presentation #2:**

“Use of RCWMs in Clause 108 RSFEC EEE”, Don Cober

See [http://www.ieee802.org/3/by/public/adhoc/architecture/cober\\_050615\\_25GE\\_adhoc.pdf](http://www.ieee802.org/3/by/public/adhoc/architecture/cober_050615_25GE_adhoc.pdf)

- Reviewed issues with RS-FEC EEE in D1.0
- Reviewed proposed solution impact to the TX and RX path.
- Discussed DC balance of the proposal. The proposal does not disturb the DC balance; the scrambling mechanism avoids the unscrambled data in the current draft.
- Mark Nowell noted that this proposal is quite similar to the next presentation and encouraged Don and Oded to work together on a consensus proposal.

### **Presentation #3:**

“RS-FEC EEE fast lock using Rapid Codeword Markers”, Oded Wertheim

See [http://www.ieee802.org/3/by/public/adhoc/architecture/wertheim\\_050615\\_25GE\\_adhoc.pdf](http://www.ieee802.org/3/by/public/adhoc/architecture/wertheim_050615_25GE_adhoc.pdf)

- Reviewed background and the RS-FEC EEE Rapid Lock issues.
- Reviewed suggest remedy. It is similar to the proposal from Don Cober.
- This proposal assumes that CWMs are present in the PCS. There is a potential conflict with the proposal to remove CWMs.
- Reviewed the list of specific changes to the draft.

### **Presentation #4:**

“Options for the RS-encoded 25GE”, Juan-Carlos Calderon

See [http://www.ieee802.org/3/by/public/adhoc/architecture/calderon\\_050615\\_25GE\\_adhoc.pdf](http://www.ieee802.org/3/by/public/adhoc/architecture/calderon_050615_25GE_adhoc.pdf)

- Received question about the term “RS10”. The term RS10 is used in the presentation to denote the RS-FEC with a symbol size of 10. OTN has used RS-FEC with a symbol size of 8 in other specifications.
- Reviewed interaction of PCS/FEC function on 1588v2.
- Received request to add a specific reference to the constant delay requirement in the IEEE 1588v2 specification.
- Several questions on PCS insertion/deletion process and specification requirements in option 2 on slide 4.
- Yong Kim and Brad Booth objected to the assumptions made on slide 5 with respect to Flex E. FlexE is still in the proposal state. Matt Brown asked them to resolve with the presenter offline.

**Presentation #5:**

“Comment #108 - Overloaded 25GBASE-CR term”, Kent Lusted

See [http://www.ieee802.org/3/by/public/adhoc/architecture/lusted\\_050615\\_25GE\\_adhoc.pdf](http://www.ieee802.org/3/by/public/adhoc/architecture/lusted_050615_25GE_adhoc.pdf)

- Brad Booth noted that he submitted a similar comment.
- There were several people interested in exploring a potential nomenclature improvement.
- Received feedback to consider timing of the changes so as not to impact the draft.
- Mark Nowell noted that the goal of the PIT meeting is to create D1.1 and pre-circulate for WG consideration at the July plenary meeting.
- Matt Brown asked participants to consider the impact of their comments on the draft and the risk to moving forward to Draft 2.0.

Matt Brown thanked the presenters for their contributions.

Next ad hoc meeting will be May 13.

The ad hoc meeting ended at 9:58 a.m. Pacific.

**List of attendees (captured from Webex tool)**

<b>Name</b>	<b>company</b>
Adam Healey	avagotech
Adee Ran	intel
Adrian Butter	us.ibm
Ali Ghiasi	independent
Andre Szczepanek	inphi
anil	brocade
Arthur Marris (Cadence)	cadence
Brad Booth	microsoft
Brian Teipen	advaoptical
Carlo Tosetti (Cisco)	cisco
Chris Roth (Molex)	molex

Dale Murray	lightcounting
Dan Cunningham	arista
Dan Dove	dovenetworking
Daniel Koehler	morethanip
David Chalupsky (Intel)	intel
David Malicoat	hp
Don Cober (CoMIRA Solutions Inc)	comira-inc
Duane Remein	huawei
Eddie Andrews	cadence
Eric Baden (Broadcom)	broadcom
Gary Nicholl	cisco
george zimmerman	Commscope & Aquantia
Greg D Le Cheminant	keysight
hfrazier	broadcom
Ingvar Froroth	marvell
Jacky Chang	hp
jean-michel	inphi
Jeff H	ranovus
Jeff Slavick	avagotech
Jim Dunford	tektronix
Jing Fang	marvell
John Dillard	microsemi
John Nelson(Arista)	arista
jonathan king	finisar
Juan-Carlos Calderon	inphi
Kapil Shrikhande	dell
Ken Van Orman	spirent
Kenneth Jackson	sei-device
Kent Lusted (Intel)	intel
Madhavi	barefootnetworks
Marco	cisco
Mark Nowell	cisco
martin white	caviumnetworks
Matt Brown	apm
Megha Shanbhag	te
Mike Andrewartha (Microsoft)	microsoft
Mike Dudek	qlogic
Myles Kimmitt	cavium
Nathan Tracy	te
Nish, Takeshi	yeu
Oded Wertheim	mellanox

Paul Kolesar	commscope
Phil Sun	marvell
Piers	mellanox
Pirooz Toyserkani	cisco
rakesh sambaraju	nexans
Randy k Rannow	independent
Raymond Nering	cisco
Rich Mellitz (Intel)	intel
Rick Rabinovich	alcatel-lucent
Rita Horner	synopsys
rob stone	broadcom
Ron Muir	jae
sambasivan	labs.att
Scott Sommers	molex
Susan Bueti	us.ibm
Thananya	ixiacom
Tom Palkert	visi
ureddy	cisco
Venu B	marvell
Vittal Balasubramanian	dell
Wei Jiang	us.ibm
Yasuo Hidaka	us.fujitsu
Yong Kim (Broadcom)	broadcom
Zvi Rechtman	mellanox