Cl **142** *SC* **142.2.2.4.2** *P* **70** *L* **11** # 596

Laubach, Mark Broadcom

Comment Type **TR** *Comment Status* **X** *post-deadline*

Comment #131 on D1.0 had a portion of it not implemented correctly:
"2) Change the box text "Information Bit
Interleaver" to "Information Bit
De-interleaver"

SuggestedRemedy

Please fix the figure accordingly

Proposed Response *Response Status* **O**

Cl **142** *SC* **142.2.2.4.2** *P* **73** *L* **20** # 595

Laubach, Mark Broadcom

Comment Type **TR** *Comment Status* **X** *post-deadline*

Need to add text to clarify shortening of upstream bursts relative to transmitted user bits.

SuggestedRemedy

Add new paragraph/note:
"Note - when the last codeword of an upstream burst is shortened, the shortening bits are at the end of the Transmitter User Bits effectively expanding the number of Zero Bits (see Figure 142-6)."

Proposed Response *Response Status* **O**

Cl **142** *SC* **142.2.2.4.5** *P* **74** *L* **8** # 594

Laubach, Mark Broadcom

Comment Type **TR** *Comment Status* **X** *post-deadline*

Need to remove potential confusion of FEC encoding versus interleaver decoding

SuggestedRemedy

Delete "encoding and"

Proposed Response *Response Status* **O**

Cl **142** *SC* **142.2.3.2.4** *P* **88** *L* **1** # 593

Kramer, Glen Broadcom

Comment Type **T** *Comment Status* **X** *post-deadline*

The generic section of the current MCRS specification allows the number of channels to onlt be powers of 2 (i.e, 1, 2, 4, 8, etc.). This is an unnecessary restriction and it does not make the MCRS state diagram simpler. MCRS is only specified this way because of how variables rCol and wCol are defined (no explict reset to 0, only on roll-over)

SuggestedRemedy

1) Define an generic constant NUM_CH to represent the number of supported MCRS channels
NUM_CH
TYPE: integer
Value: application specific (see 143.3.3.2)
The NUM_CH constant represents the number of channels supported by an MCRS-based device.

2) In Nx25G-EPON application-specific section, add the following:
NUM_CH
Value: 1 for devices supporting only 25 Gb/s operation over a single channel;
2 for devices supporting 50 Gb/s operation over two channels.

2) Make changes to MCRS Input (Fig 143-13) and MCRS Output (Fig 143-17) state diagrams as shown in red in kramer_3ca_5_0918.pdf.

Proposed Response *Response Status* **O**

Cl **142** *SC* **142.2.3.2.4** *P* **88** *L* **1** # 592

Kramer, Glen Broadcom

Comment Type **T** *Comment Status* **X** *post-deadline*

In ONU synchronizer state diagram (Figure 142-16), FecDecoded variable needs to be defined as a three-valued logic: (true - successful decoding; false - failed decoding, Z - waiting for FEC decoding. This is an unnecessary complicated solution to a simple behavior.

SuggestedRemedy

Revert to the original version of this state diagram (kramer_3ca_1_0318.pdf) and use two booleans: FecDecodeSuccess and FecDecodeFailure.
The changes to the state diagram and the definitions of these two variables are provided in kramer_3ca_4_0918.pdf.

Proposed Response *Response Status* **O**