Figure 141–3—P2MP timing parameter definition, per channel

141.7.14 Receiver settling timing measurement

$T_{rx\_settling}$ is defined in 141.7.14.1 and has a value of less than 800 ns (defined in Table 141–15 and Table 141–16). A method for measuring $T_{rx\_settling}$ is illustrated in 141.7.13.2 (informative).

141.7.14.1 Definitions

$T_{rx\_settling}$ is defined to be the time between the moment when the optical power at TP7 reaches the conditions specified in 141.7.11 and the moment after which the electrical modulation (peak-to-peak) at TP8[i] remains within 15% of its steady state amplitude and T~~receiver_settling is denoted as the elapsed time beginning from the moment that the optical power in the receiver at TP7 reaches the conditions specified in 141.7.11 and ending at the moment that the electrical signal after the PMD at TP8[i] reaches within 15 % of its steady state average power,~~ jitter (see Table 141–15 and Table 141–16[TBD]). The ~~T receiver~~$T_{rx\_settling}$ ~~is presented~~time interval is illustrated in Figure 141-3[-TBD-]. The data transmitted may be any valid 256B/257B symbols (or a specific power synchronization sequence). The optical signal at TP7, at the beginning of the locking, may have any valid 256B/257B pattern, optical power level, jitter, or frequency shift matching the standard specifications.