

**[A] Comment #106 - Proposed Draft Text****142.2.2.2.6 Differential Encoder [Comment**

Differential encoding as shown in Fig. X shall be implemented in the OLT TX PCS for downstream.

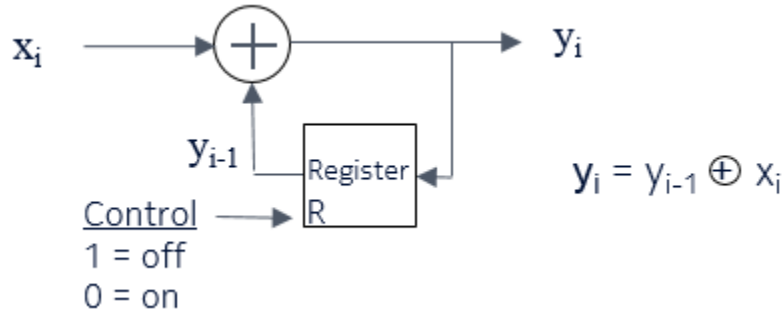


Figure X

$X_i$  = input from OLT PCS FEC encoder

$Y_i$  = output to OLT PCS gearbox

A control bit shall be added to be able to disable the differential encoding function via SW provisioning.

**[B] Comment #107 - Proposed Draft Text****142.2.3.1 Differential Decoder (or possibly in CL 142.2.3.2 since this function is for the ONU)**

Differential decoding shall be implemented in the ONU PCS RX function as shown in Figure Y

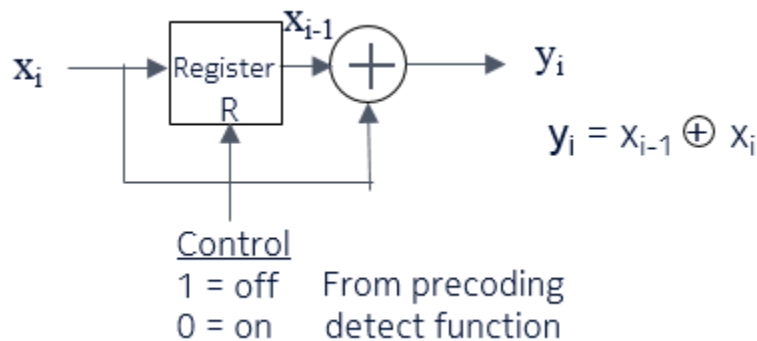


Figure Y

$X_i$  = Input from ONU PMA

$Y_i$  = Output to ONU PCS Synchronizer

The ONU shall implement automatic detection of RX path differential encoding, and switch the decoder in appropriately.