

IEEE P802.3cd Ad Hoc Report

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Ad Hoc Chair

Report

- Charter: to discuss topics towards supporting the developed objectives, PAR and CSD or for providing the Task Force with contributions to help it prepare for future decisions.
- 6 ad hoc call(s) since November 2017 meeting
 - [2017: Nov 22, Nov 29, Dec 6, Dec 20] and [2018: Jan 10, Jan 17]
 - 50+ attendees
 - 14 contributions presented (see next slide)
- Meeting minutes and presentation materials:
<http://www.ieee802.org/3/cd/public/adhoc/archive/index.html>
- Further work: continue to refine content towards future drafts and address gaps in the specification
- Next meeting: To be announced over the email reflector (Assume February 7 @ 8am Pacific)

Presentations

- General
 - “Key Focus Areas - P802.3 Sponsor Ballot”, Mark Nowell
- Electrical
 - “Impact of Test Fixtures on ERL, RL, SNRISI using COM and with Package Product Examples”, Rich Mellitz
 - “AC coupling and impedances in Tx/Rx electrical test setups”, Adee Ran
 - “Moving Forward with ERL”, Rich Mellitz
 - “Observations of ERL analysis ”, Upen Reddy Kareti, et al.
 - “Transmitter Specifications and COM for 50GBASE-CR”, Mike Dudek, et al.
 - “Jitter considerations for 100GAUI-2 with 100GBASE-DR, comments i-61 and i-115”, Adee Ran, et al.
- Optical
 - “802.3cd: proposed change in TDECQ method method and reference receiver equalizer ”, Matt Traverso, et al.
 - “50 Gb/s PAM4 VCSEL results”, Jonathan King
 - “RLM vs. PAM4 Threshold Adjustment”, Hai-Feng Liu
 - “TDECQ updates with threshold Adjustment: VCSEL Results”, Frank Chang, et al.
 - “Proposal to modify the SRS test for 100GBASE-DR”, David Lewis
 - “TDECQ : RLM and threshold adjust”, Jonathan King
 - “SECQ Test Method and Calibration Improvements”, Scott Schube, et al.

THANKS!