Baseline proposal for "Alignment Marker mapping to FEC lanes" for 50GbE and NG 100GbE

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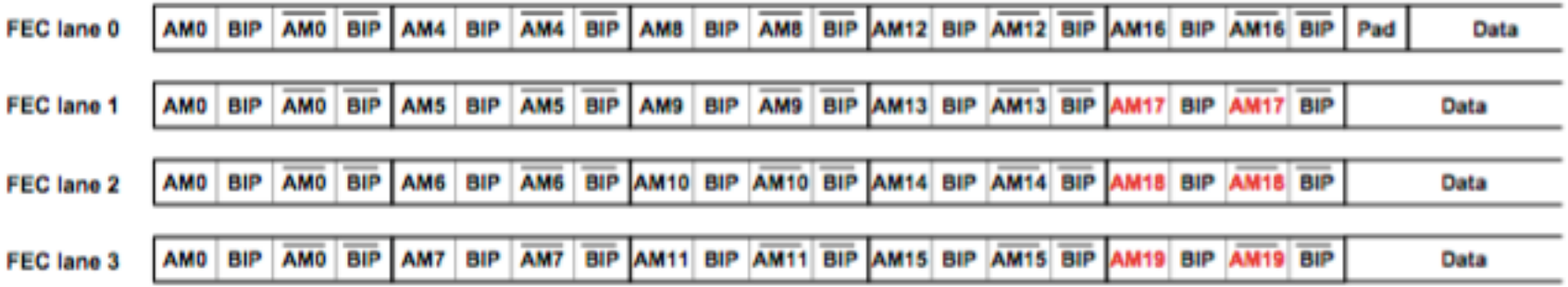
Background

- In San Diego we agreed to adopt nicholl_3cd_01a_0716 as the baseline for the 50Gb/s and 100Gb/s RS/MII, PCS, FEC and PMA
- However the details of the AM (alignment marker) to FEC lane mapping for both 50GbE and 100GbE were left as TBD. Specifically it was recognized that the Clause 91 mapping may need to be modified to avoid clock content issues when bit muxing FEC lanes
- anslow_3cd_02_0716 analysed the performance of the alignment markers for 100GbE, and identified a candidate AM mapping which appears to have sufficient performance with respect to both baseline wander and clock content.

Proposal

- Adopt the candidate AM mapping identified in anslow_3cd_02_0716 (slide 23) as the baseline for 100GbE
- Adopt an AM mapping based on the approach taken in anslow_3cd_02_0716 (slide 23) as the baseline for 50GbE (with an understanding that further analysis will be carried out to verify the performance)

AM mapping proposal for 100GbE



Red Text: Modification from Clause 91

Source: anslow_3cd_02_0716 (slide 23)

AM mapping proposal for 50GbE