Agenda for the P802.3cd TF ad hoc meeting, May 16, 2018

Meeting

- Attendance
  - Attendee names and affiliations for these ad hoc meetings will be taken from the WebEx participants list.
  - Please ensure that your full name and employer/affiliation are indicated correctly.
  - If the WebEx participants list does not correctly indicate your name and employer/affiliation, please send an e-mail to the ad hoc recording secretary:
    Kent Lusted (kent dot c dot lusted at intel dot com).
- WebEx
  - Please mute your lines if you are not speaking
    - *6 to mute/unmute or click on mute button in the WebEx window
  - Noisy, unmuted lines will be muted by the WebEx organizer
    - *6 to unmute if this happens to you
- Participants wishing to contact the presenter can contact the Chair for their contact info

P802.3cd Task Force ad hoc communications

- The TF ad hoc web page is here:
  - http://www.ieee802.org/3/cd/public/adhoc/index.html
  - Includes ad hoc meeting schedule and WebEx information.
- All e-mail communications are via the TF reflector
  - archive and subscription links are here:
    http://www.ieee802.org/3/cd/reflector.html

Proposed agenda

- Approve agenda
- Approve 9 May 2018 ad hoc minutes
- Patent reminder
  - http://www.ieee802.org/3/patent.html
- Participant reminder
- P802.3cd TF
  - TF update - Mark Nowell (10 mins)
  - "ERL update" – Mike Dudek (20 mins)
  - "TDECQ and SRS" – Piers Dawe (20 mins)
  - "Tx constraints" – Jonathan King (20 mins)
  - "100GBASE-DR: GoldenEye" – Marco Mazzini (20 mins)

Participation in IEEE 802 Meetings

Participation in any IEEE 802 meeting (Sponsor, Sponsor subgroup, Working Group, Working Group subgroup, etc.) is on an individual basis

- **Participants in the IEEE standards development individual process shall act based on their qualifications and experience. (https://standards.ieee.org/develop/policies/bylaws/sb_bylaws.pdf section 5.2.1)**

- **IEEE 802 Working Group membership is by individual; "Working Group members shall participate in the consensus process in a manner consistent with their professional expert opinion as individuals, and not as organizational representatives". (subclause 4.2.1 "Establishment", of the IEEE 802 LMSC Working Group Policies and Procedures)**

- **Participants have an obligation to act and vote as an individual and not under the direction of any other individual or group. A Participant's obligation to act and vote as an individual applies in all cases, regardless of any external commitments, agreements, contracts, or orders.**

- **Participants shall not direct the actions or votes of any other member of an IEEE 802 Working Group or retaliate against any other member for their actions or votes within IEEE 802 Working Group meetings, see https://standards.ieee.org/develop/policies/bylaws/sb_bylaws.pdf   section 5.2.1.3 and the IEEE 802 LMSC Working Group Policies and Procedures, subclause 3.4.1 "Chair", list item x.**

By participating in IEEE 802 meetings, you accept these requirements. If you do not agree to these policies then you shall not participate.
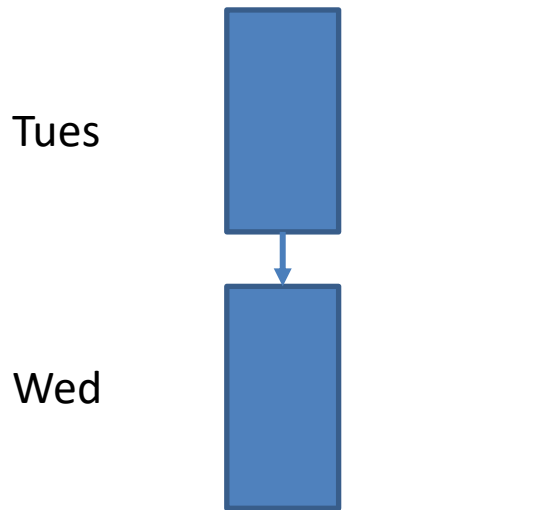
(Latest revision of IEEE 802 LMSC Working Group Policies and Procedures: http://www.ieee802.org/devdocs.shtml)

Presentation Requests – full schedule

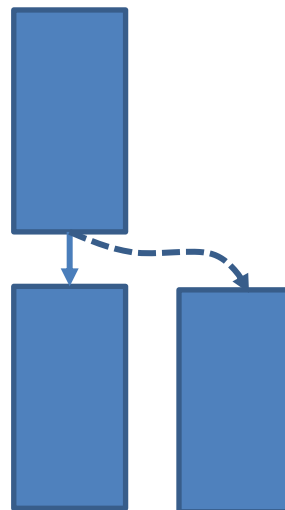|			
Mark Nowell	Cisco	Agenda and General Information	agenda_3cd_01_0518.pdf
Kent Lusted	Intel	802.3cd Task Force Ad-hoc report	lusted_3cd_01_0518.pdf
Matt Brown	Macom	802.3cd Chief Editor's Report	brown_3cd_01_0518.pdf
Matt Brown	Macom	Comment agenda	brown_3cd_02_0518.pdf
Phil Sun	Credo	TDECQ Reference Receiver Precursor Constraint	sun_3cd_01_0518.pdf
Rich Mellitz	Samtec	ERL Change Proposal Overview	mellitz_3cd_01_0518.pdf
Piers Dawe	Mellanox	TDECQ and SRS	dawe_3cd_01_0518.pdf
Piers Dawe	Mellanox	SNDR observation bandwidth	dawe_3cd_01_0518.pdf
Toshiaki Sakai	Socionext	ERL measured results for 50G-KR Device (update)	sakai_3cd_01_0518.pdf
Pavel Zivny	Tektronix	SECQ and its sensitivity to measurement bandwidth	zivny_3cd_01_0518.pdf
Mike Dudek	Cavium	ERL and system performance for 50GBASE-CR	dudek_3cd_01_0518.pdf
Adee Ran	Intel	ERL effect on COM (in support of r02-30)	ran_3cd_01_0518.pdf
Jonathan King	Finisar	Changes to other optical specs when TDECQ spec limit is adjusted	king_3cd_01_0518.pdf
Jonathan King	Finisar	Additional PAM4 transmitter constraints	king_3cd_02_0518.pdf
Jonathan King	Finisar	VCSEL transmitter performance update	king_3cd_03_0518.pdf
Marco Mazzini	Cisco	100GBASE-DR: GoldenEye	mazzini_3cd_01_0518.pdf

Approximately: 5 ½ to 6 hrs to review if full time taken

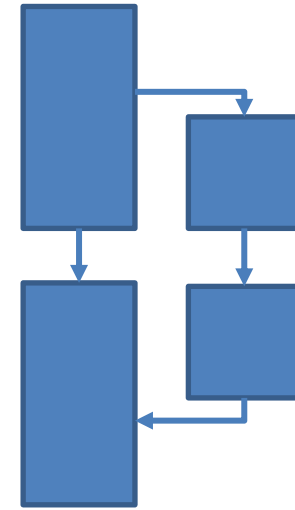# Planning options

Tues

Wed

Option 1
All in one track
Presentations all day Tues
Tight on Wed to finish 61 comments

Option 2
2nd track available on Wed
All presentations on Tues(?)
Can't be in both rooms

Option 3
Geneva/Rosemont approach
Run as separate tracks
Can't be in both rooms